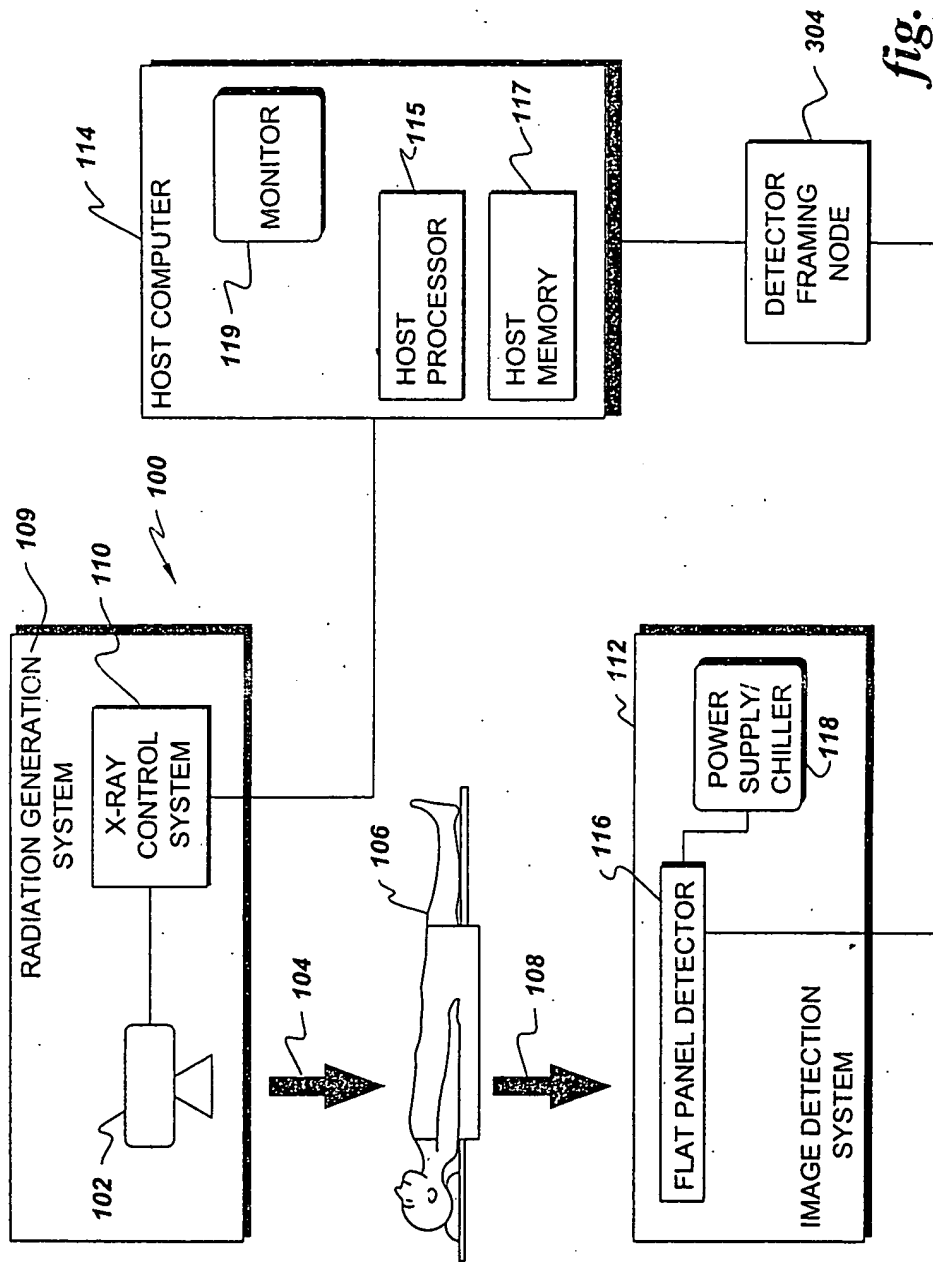


102000-0154200



102050-6154260

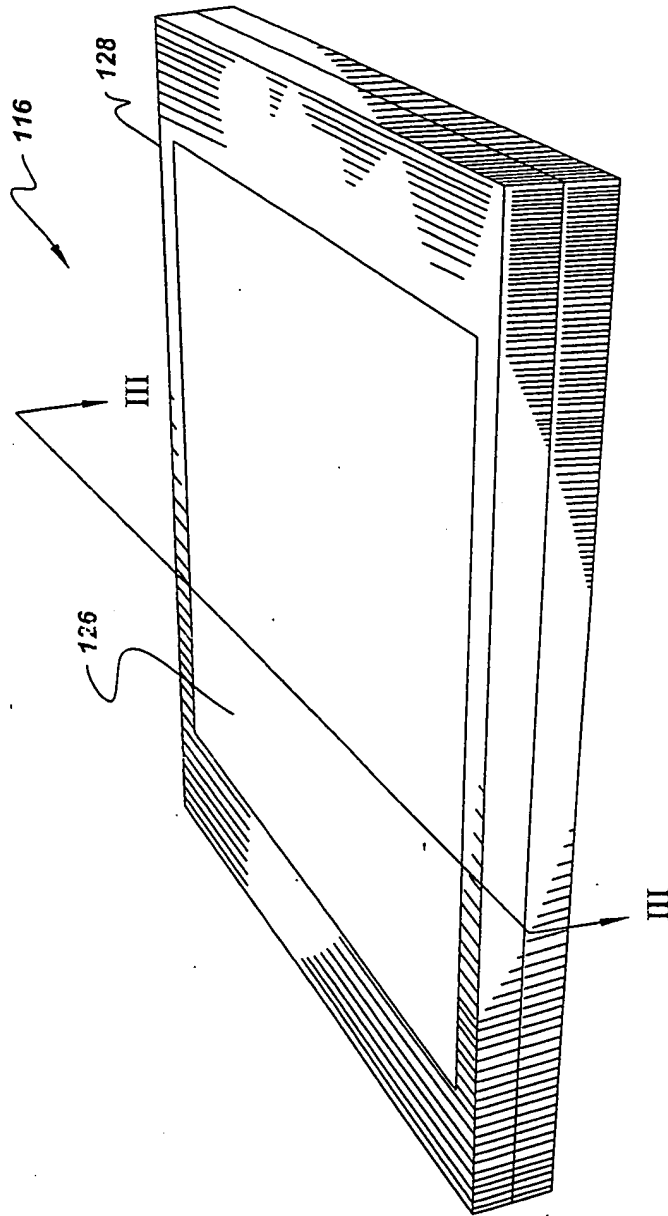


fig. 2
(PRIOR ART)

TO: 050" 64514260

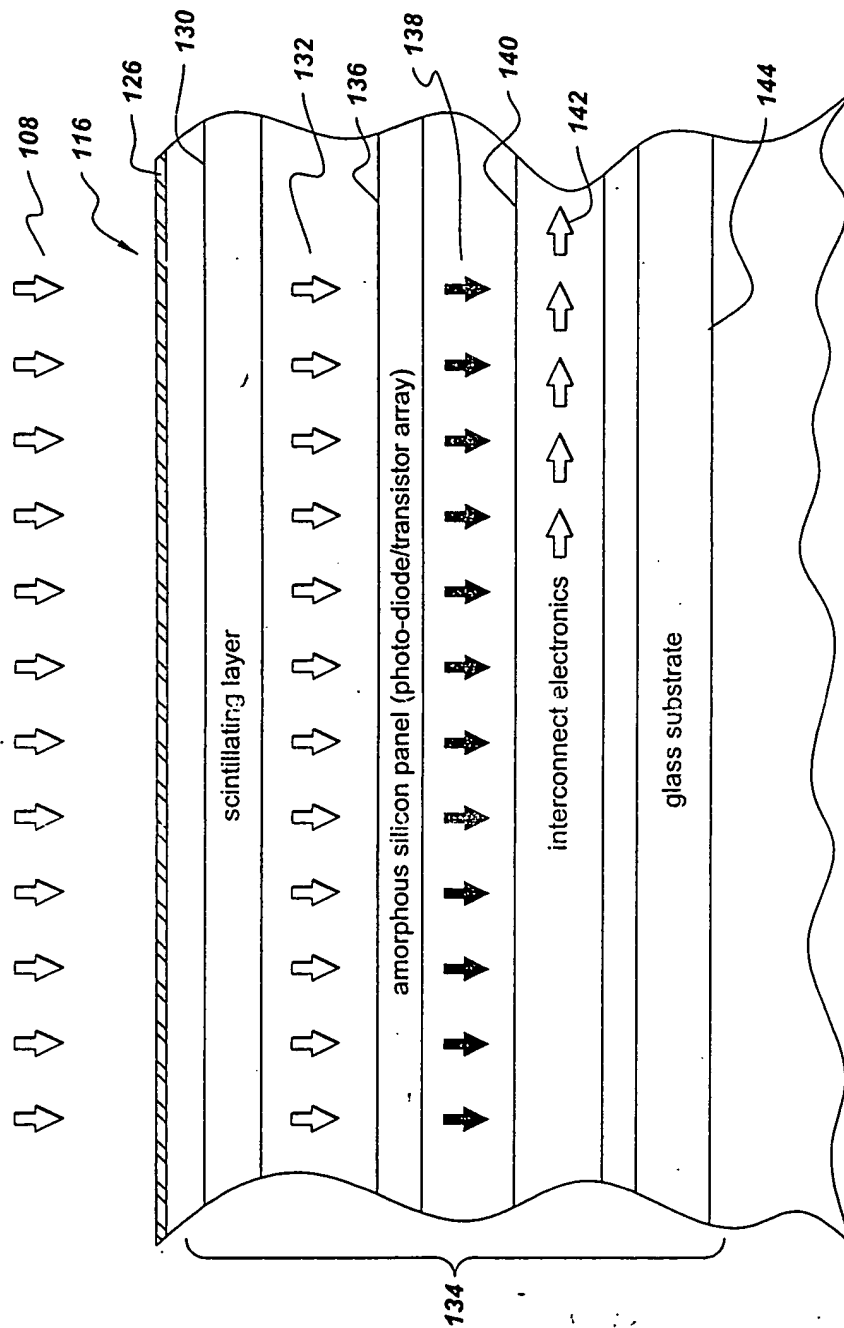


fig. 3

102050-6451260

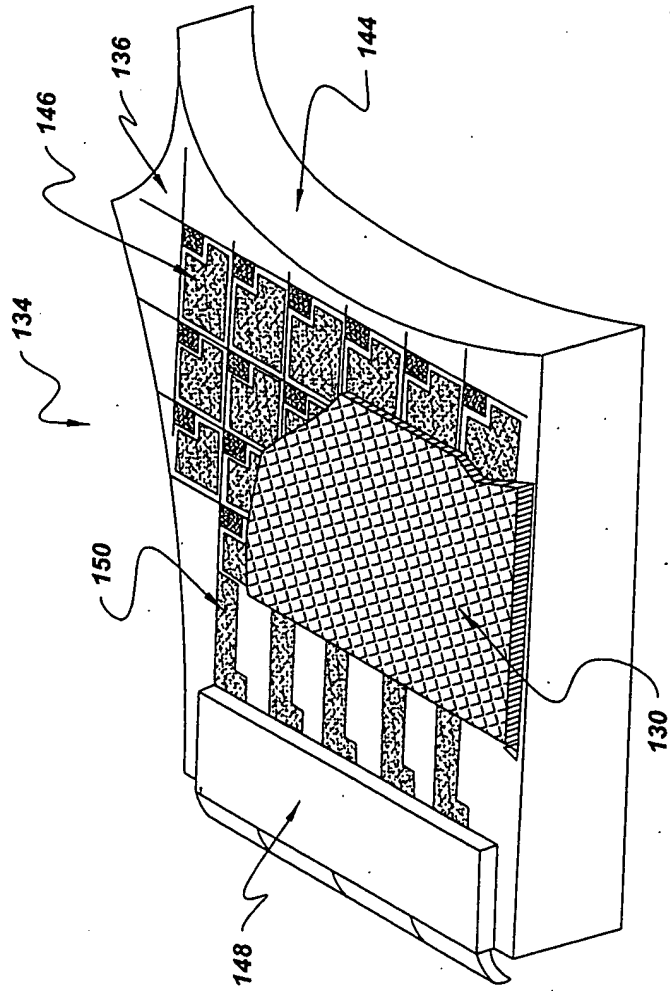


fig. 4
(Prior Art)

FIG. 5

152

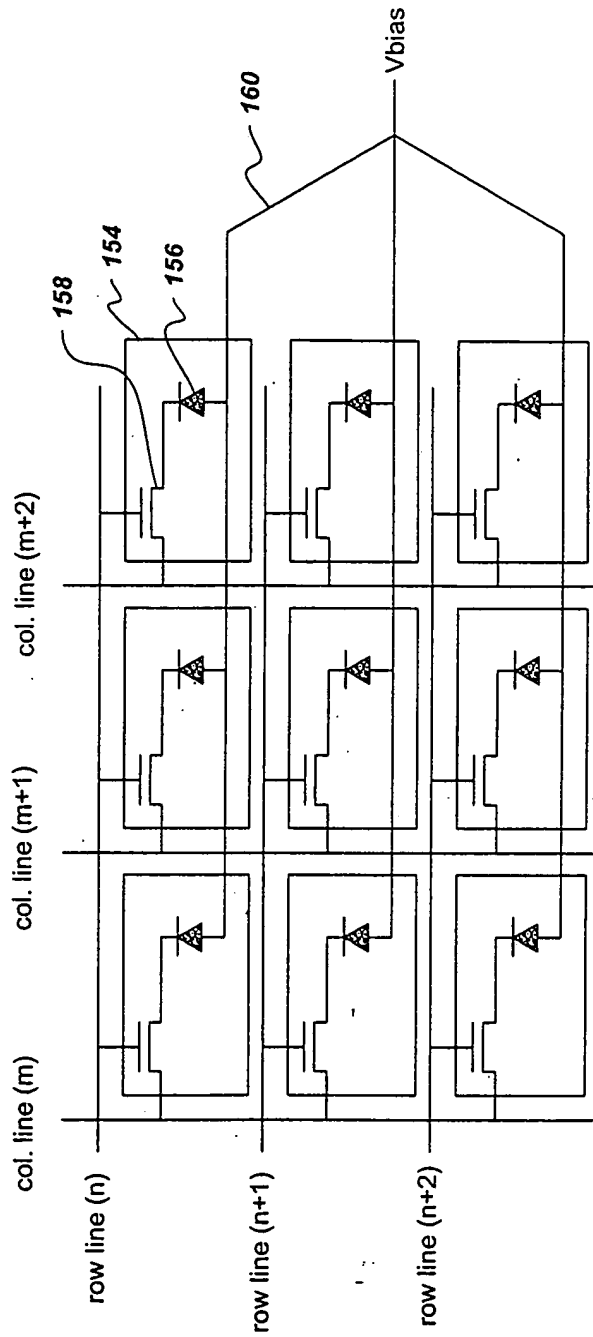


fig. 5
(Prior Art)

6/53

0974549-050701

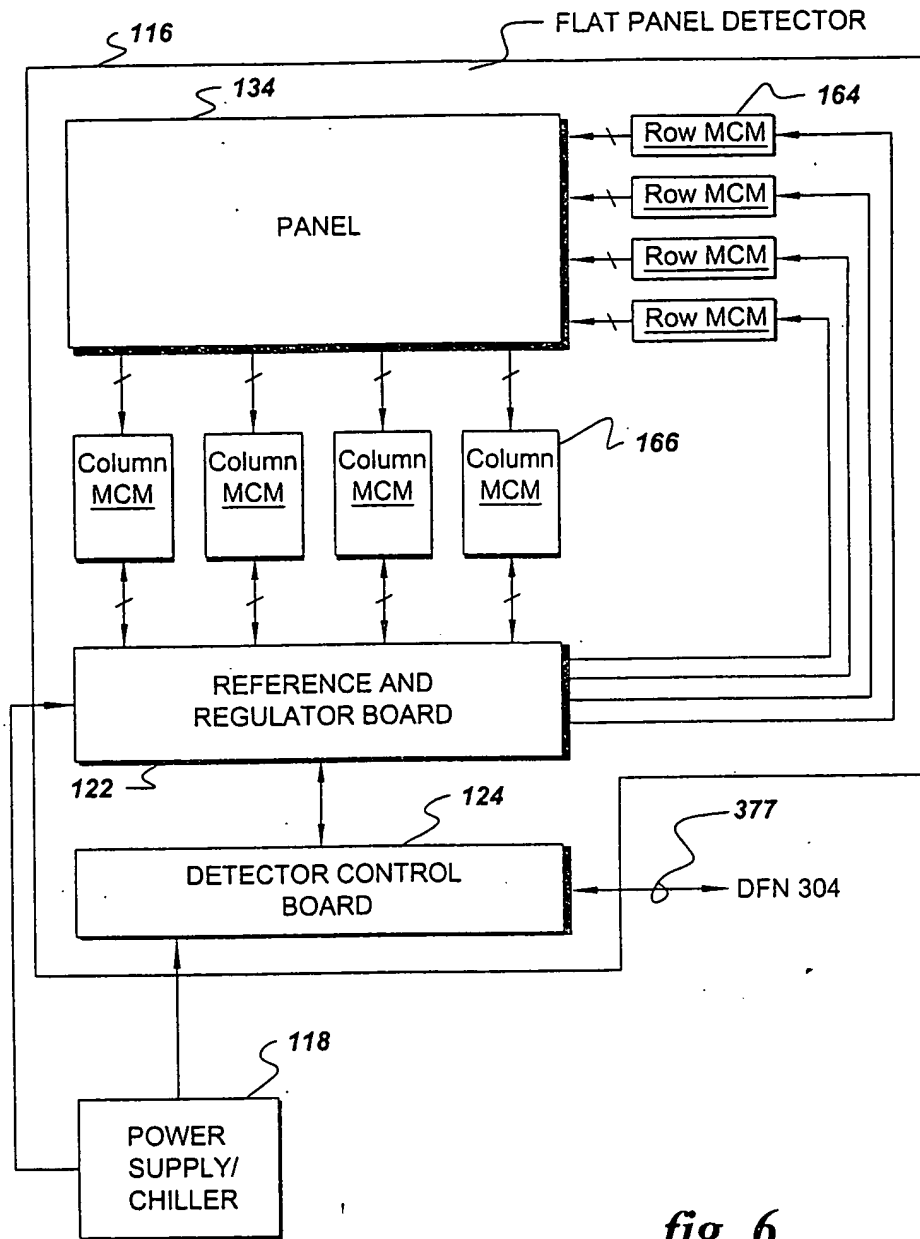


fig. 6
(PRIOR ART)

7/53

FLAT PANEL DETECTOR

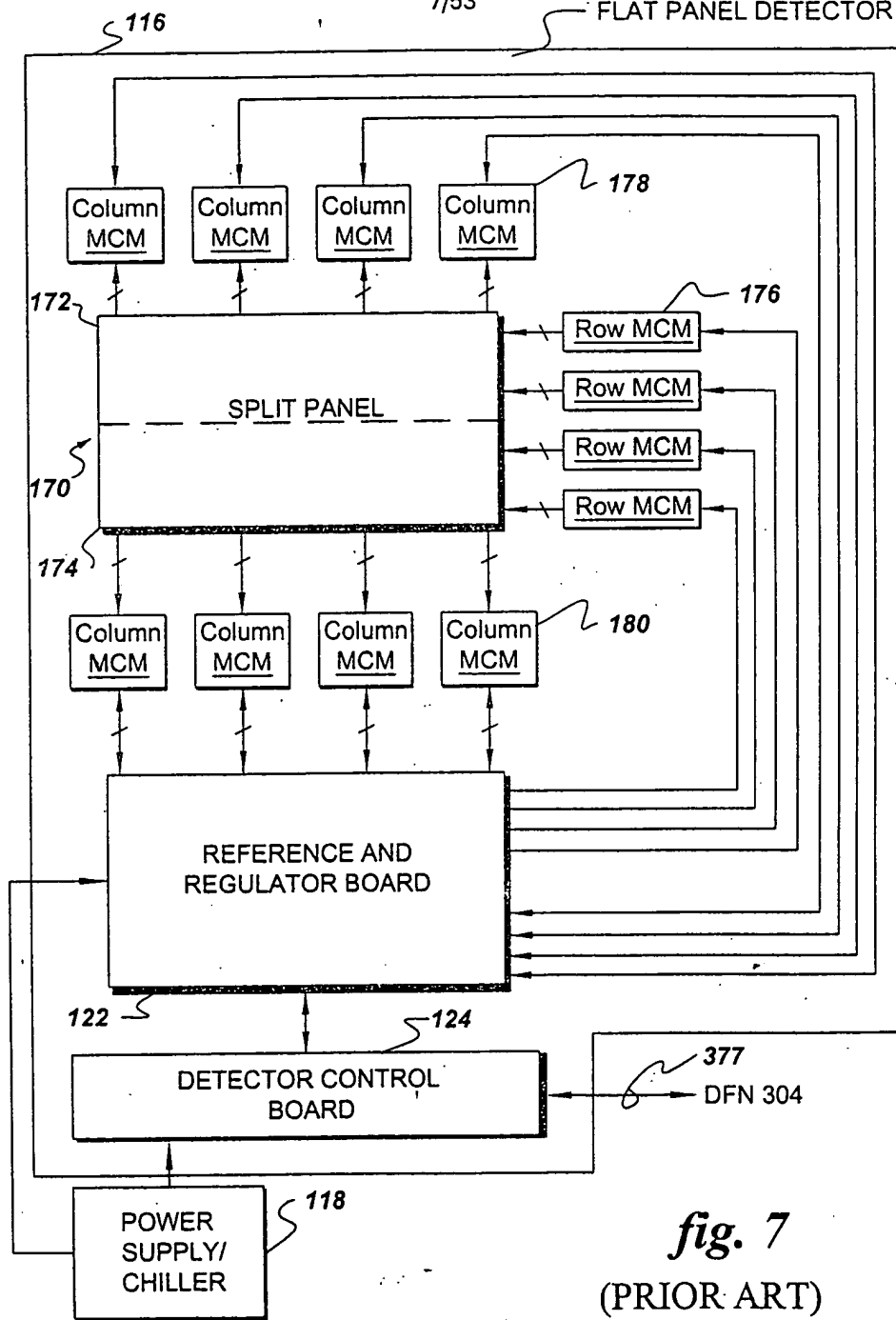
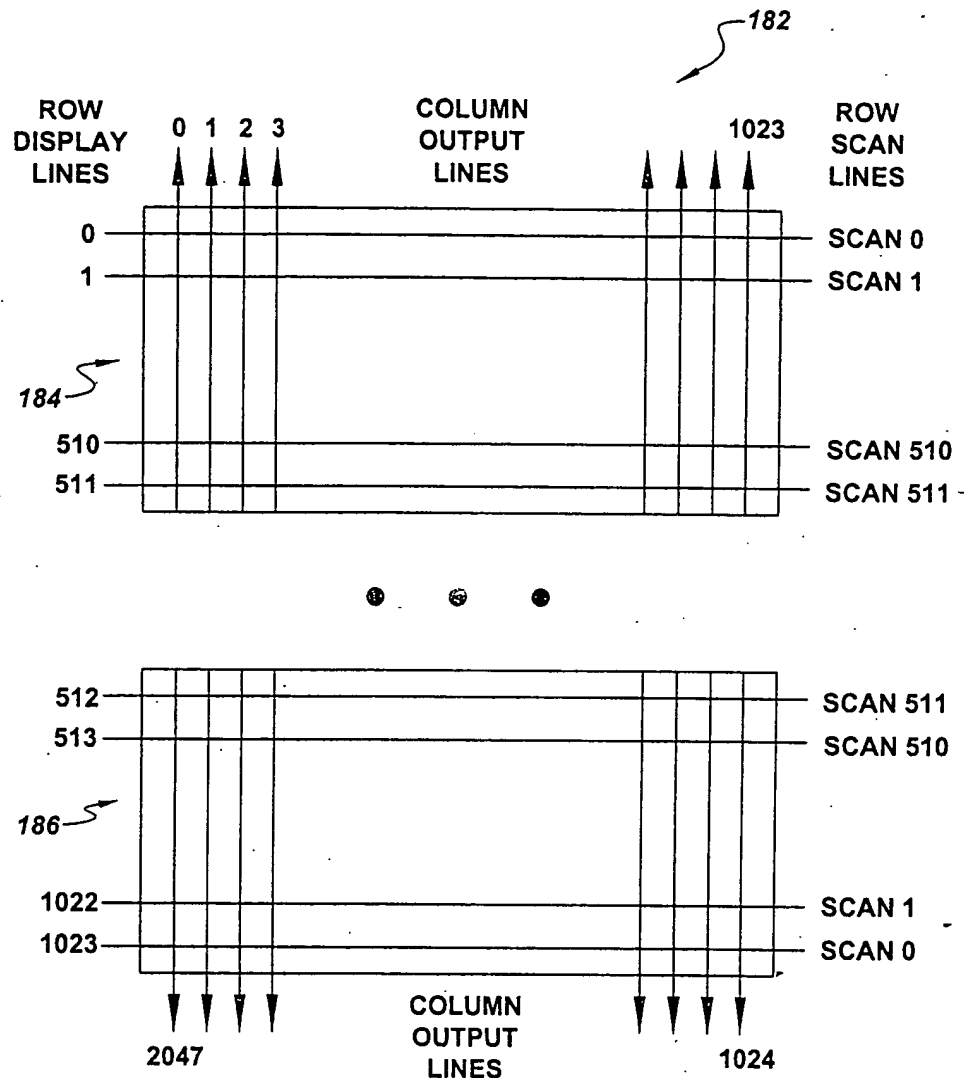


fig. 7
(PRIOR ART)

09771549 050704



CARDIAC/SURGICAL DIGITAL X-RAY PANEL

fig. 8
(PRIOR ART)

9/53

03771519 "050704

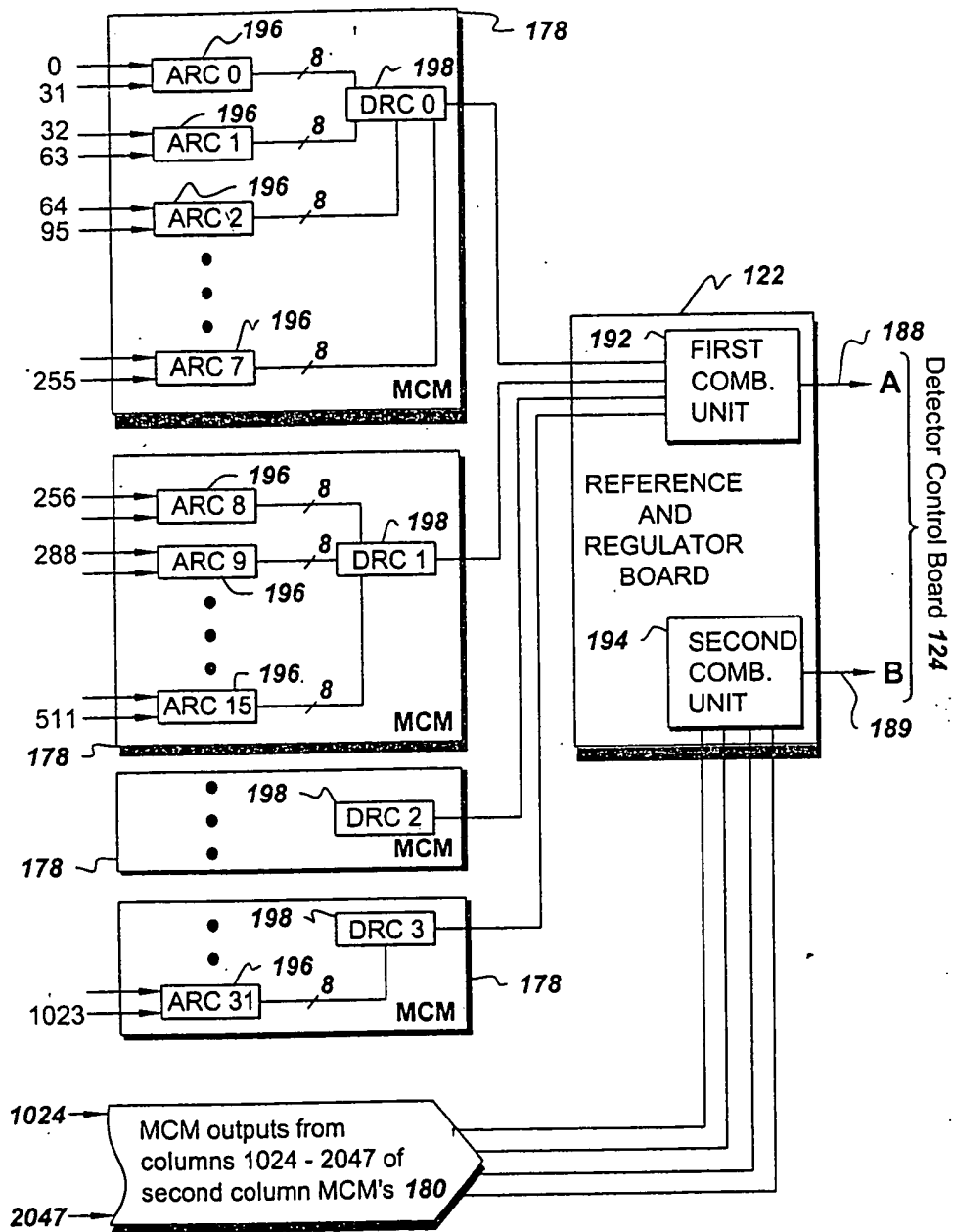
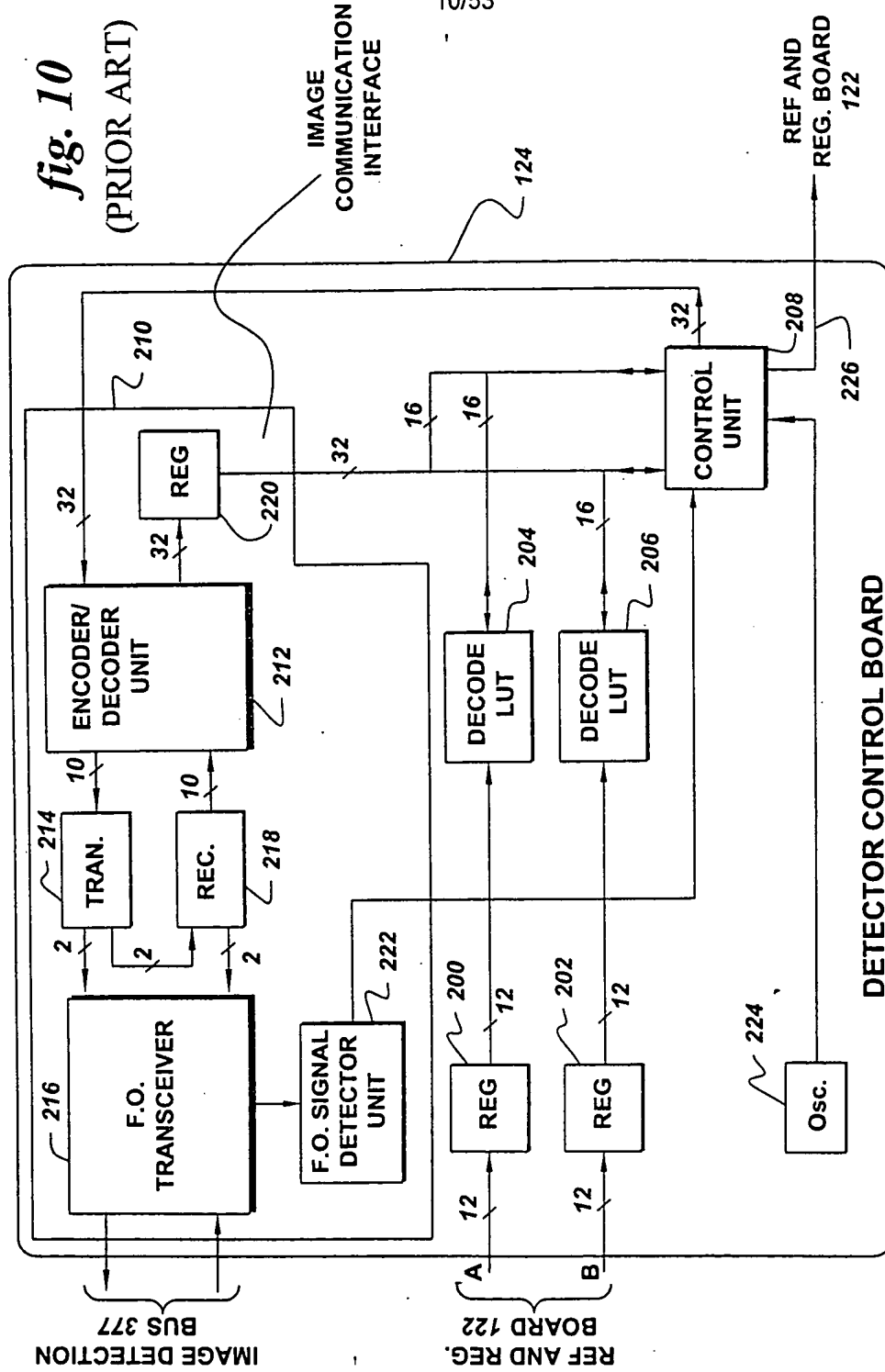


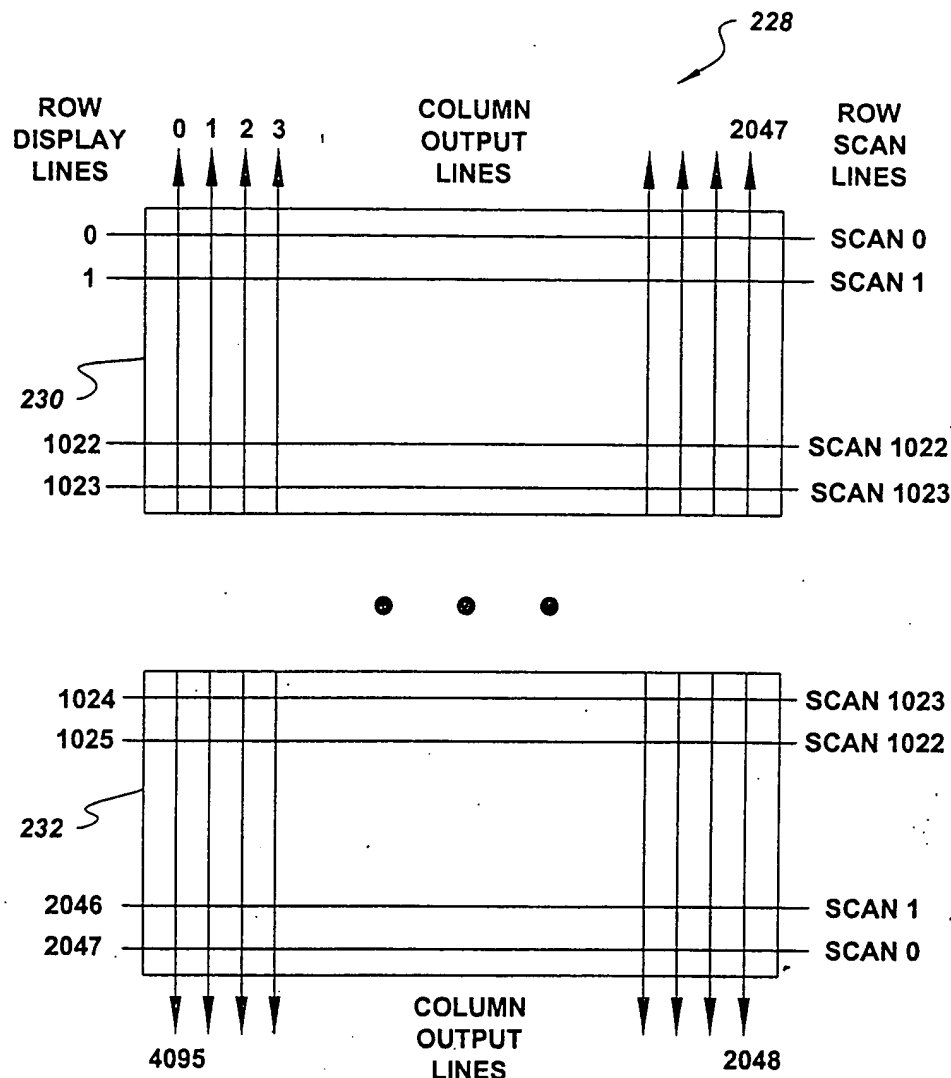
fig. 9 (PRIOR ART)

10/53

fig. 10
(PRIOR ART)



11/53



RADIOGRAPHY DIGITAL X-RAY PANEL

fig. 11 (PRIOR ART)

00774549 000701

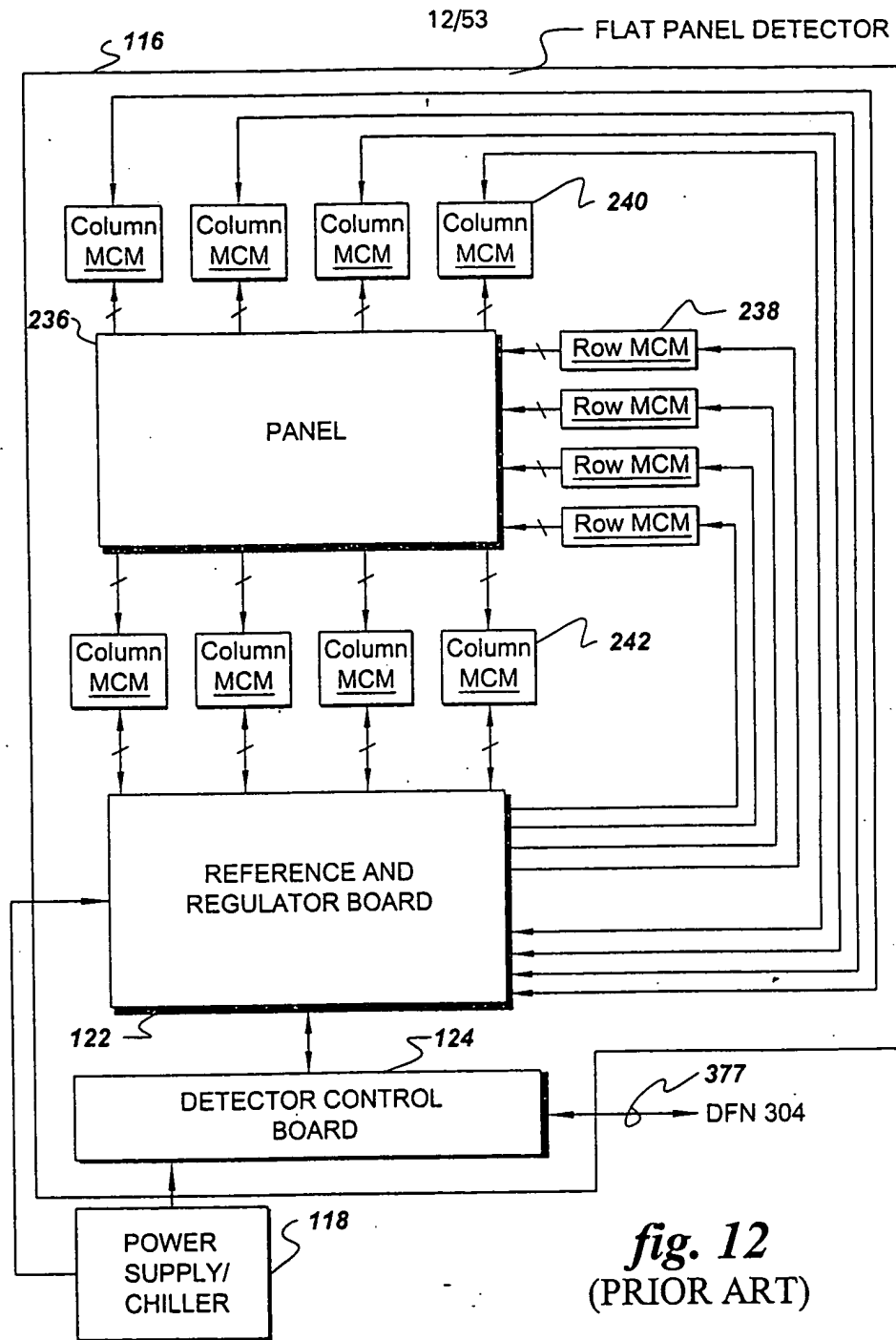
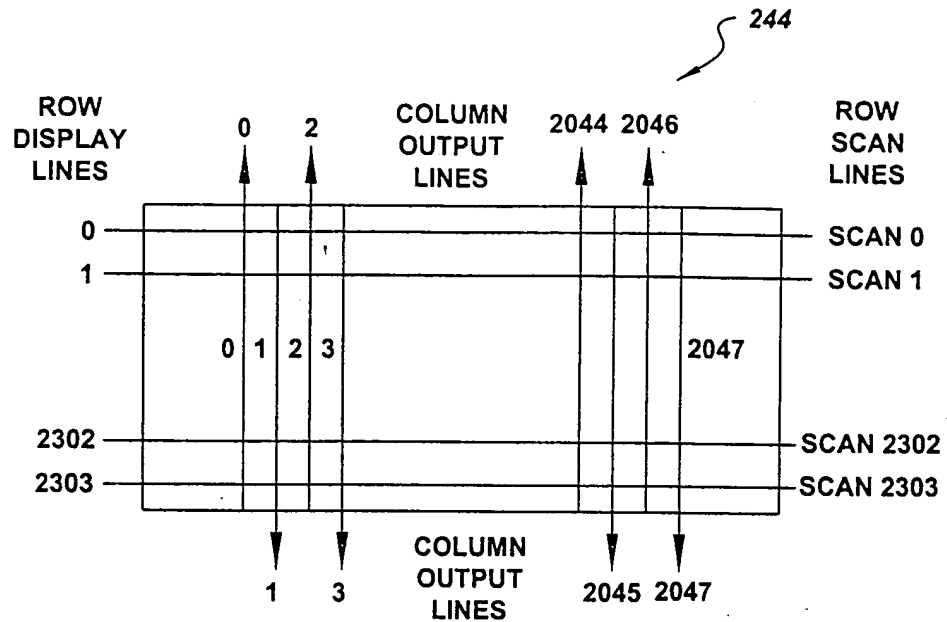


fig. 12
(PRIOR ART)

13/53



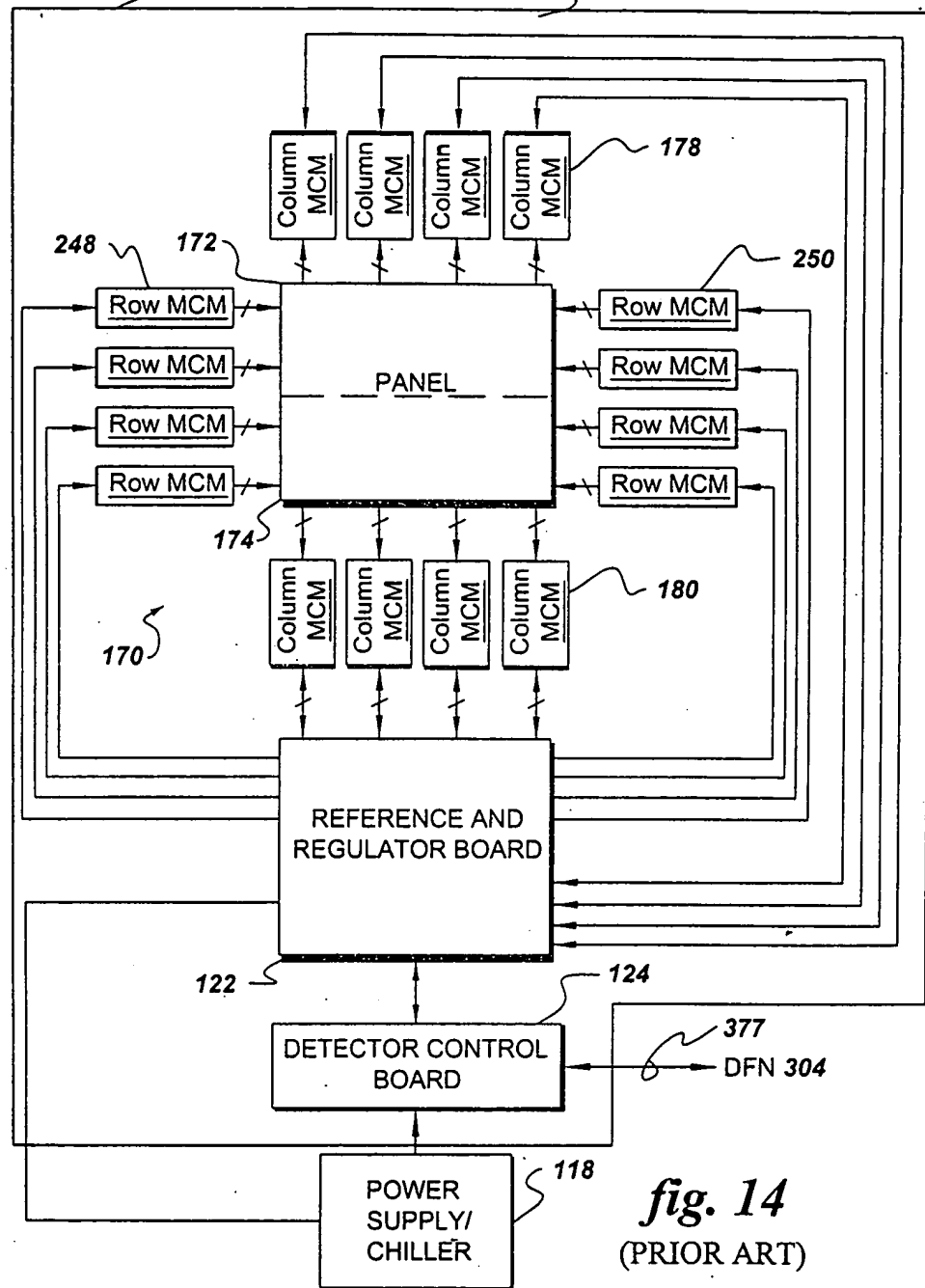
MAMMOGRAPHY DIGITAL X-RAY PANEL

fig. 13
(PRIOR ART)

00711549-050701

14/53

FLAT PANEL DETECTOR



0974549-050701

15/53

114

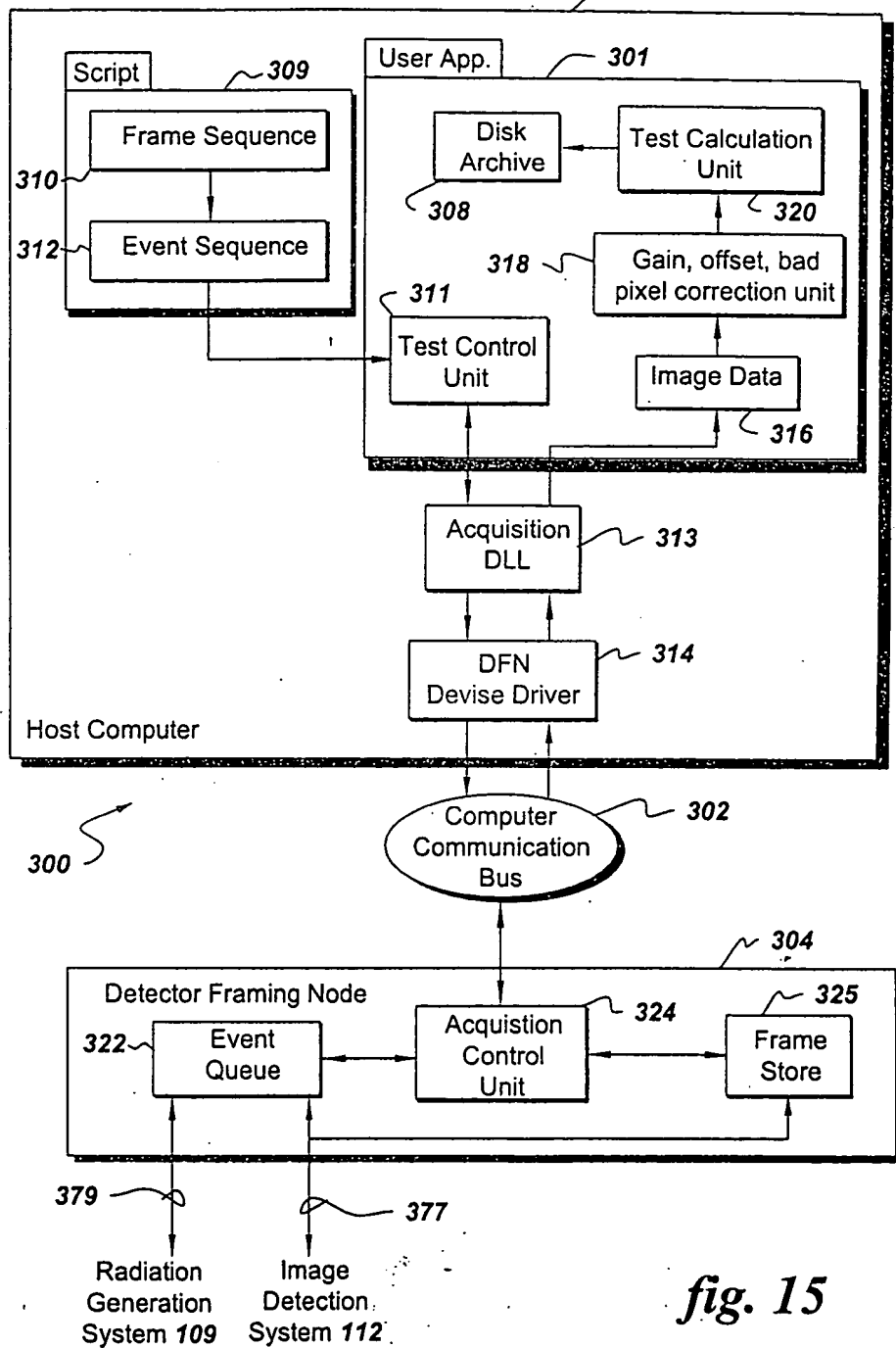


fig. 15

FIG. 16

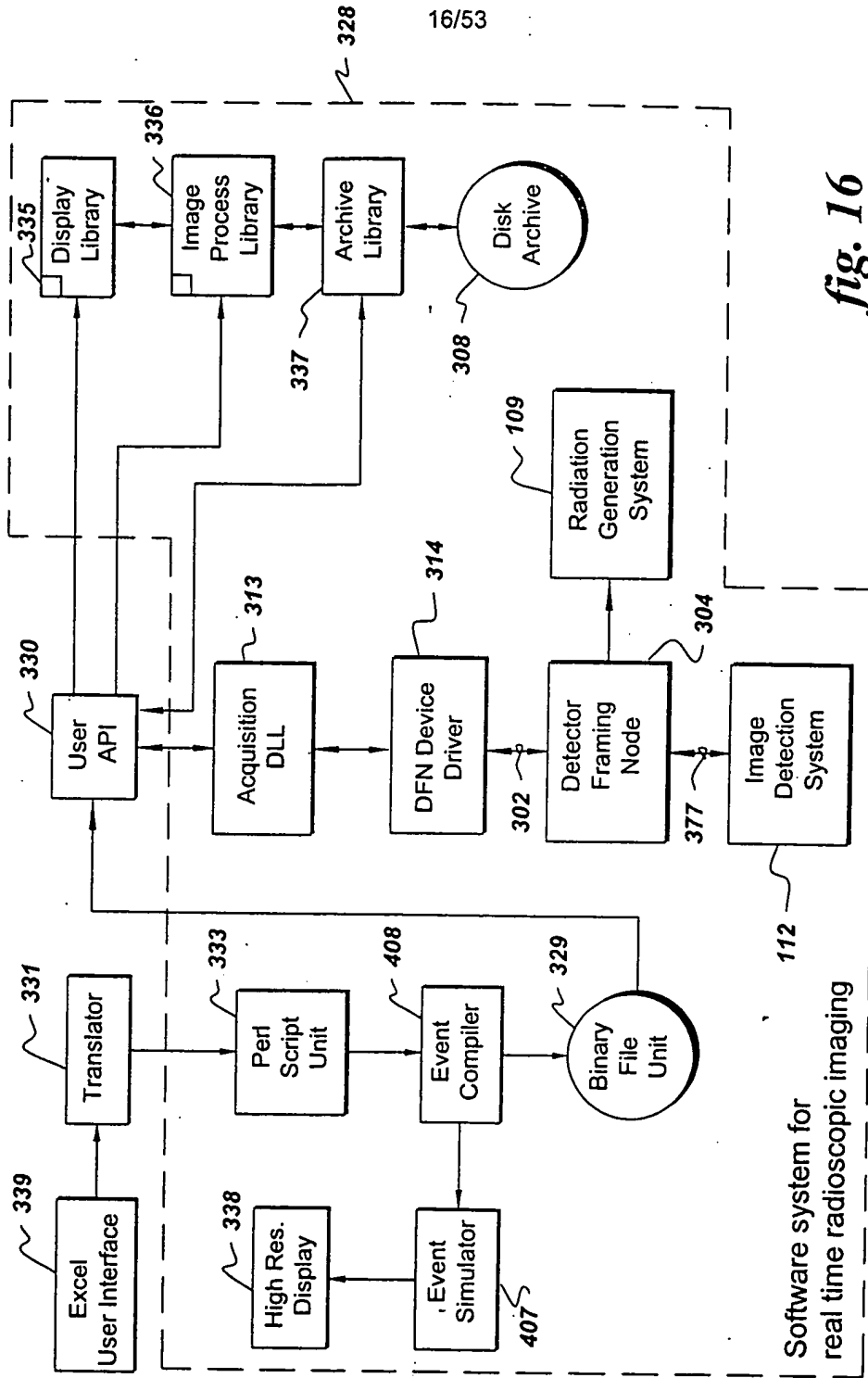
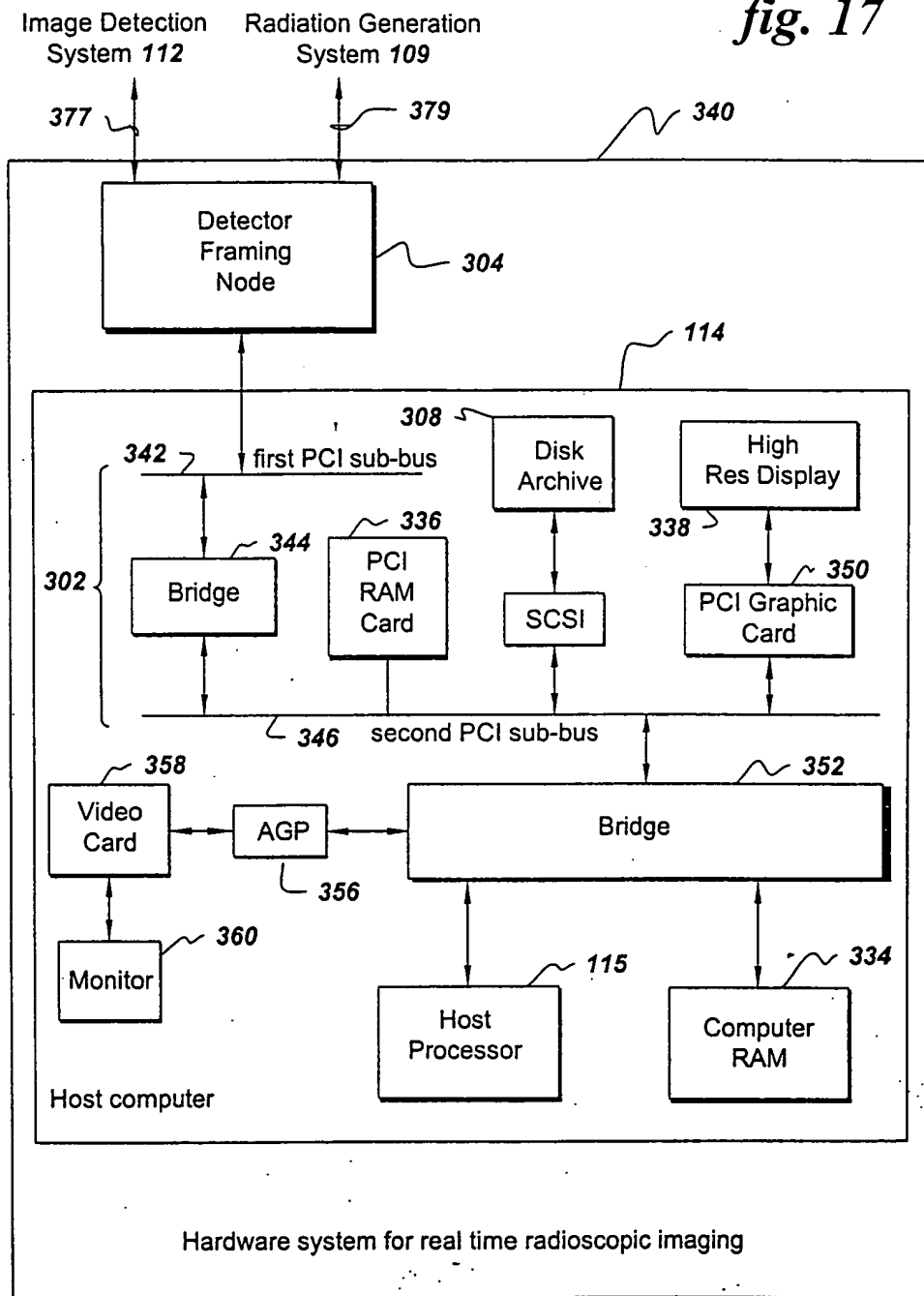


fig. 16

17/53

fig. 17



05741549.050704

18/53

FIG. 18

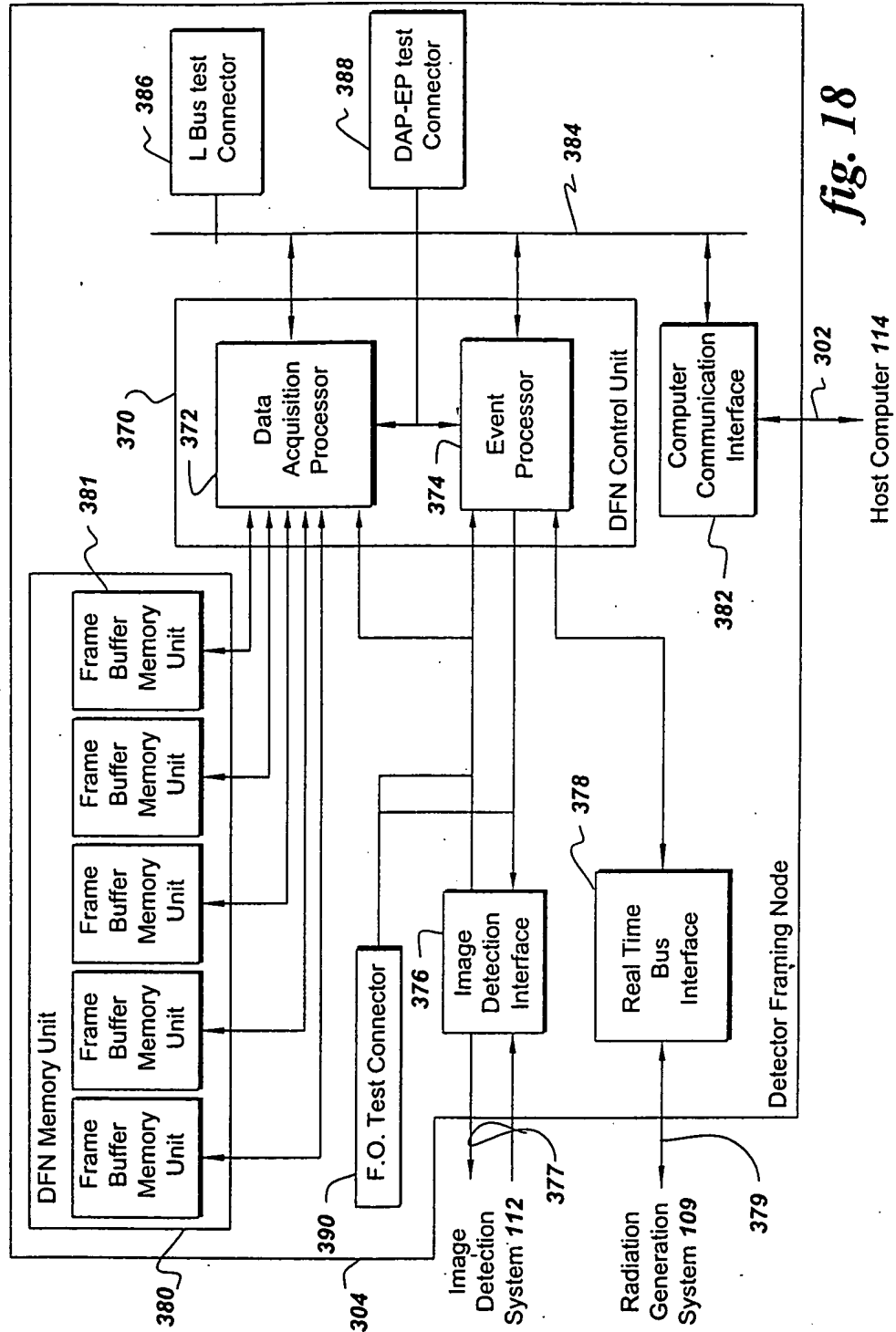


fig. 18

19/53

FD-2050" 6/15/14/60

	(fm/sec)	length	latency	memory offset	gbr
Panel Setup	Real Time	30	unlimited	host	none
Single Frame	Post Process	-	Delay ~.1 sec	"	y
Single Frame	Post Process	-	Delay ~.2 sec	"	y
Real Time	Real Time	R	<5 frames	host	none
Real Time	Real Time	R-X	<5 frames	"	y
Real Time	Real Time	R-Y	<5 frames	"	y

fig. 19

Modality	image size	Frames Stored host memory
Cardiac	1024 X 1024	200
Rad	2048 X 2048	50
Mammo	2304 X 2048	44

fig. 20

20/53

09741549-050701

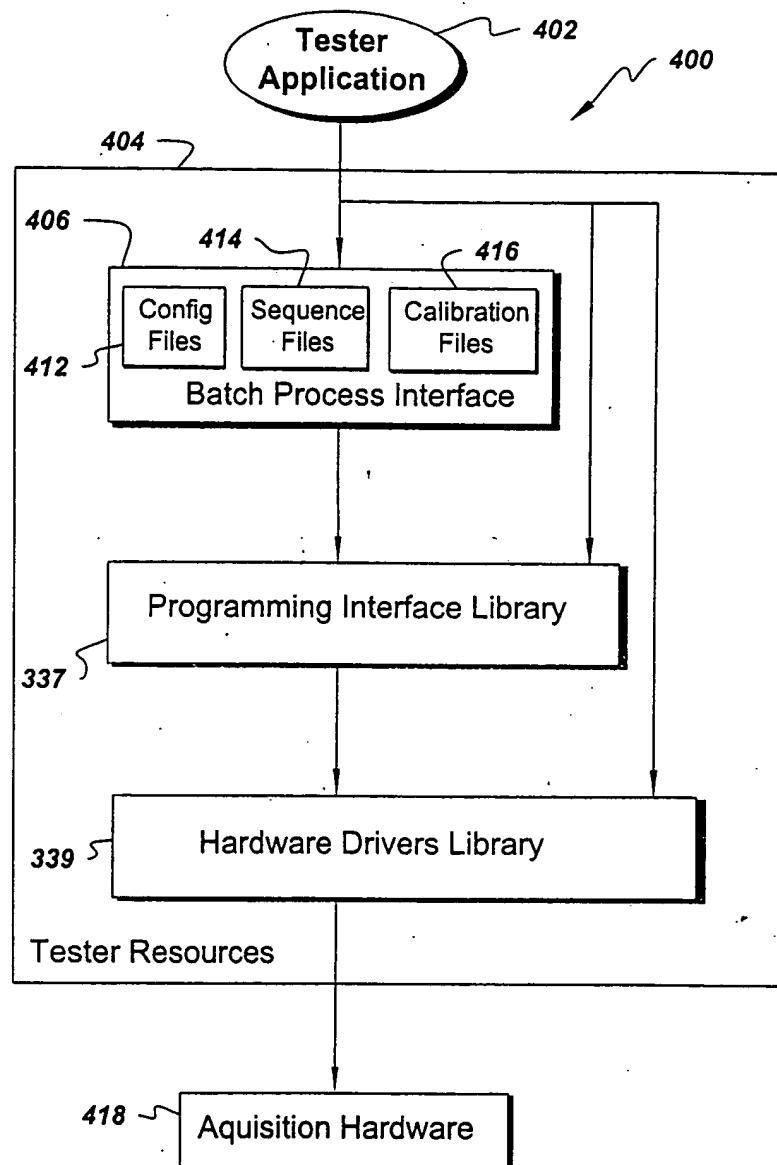


fig. 21

FIG. 22

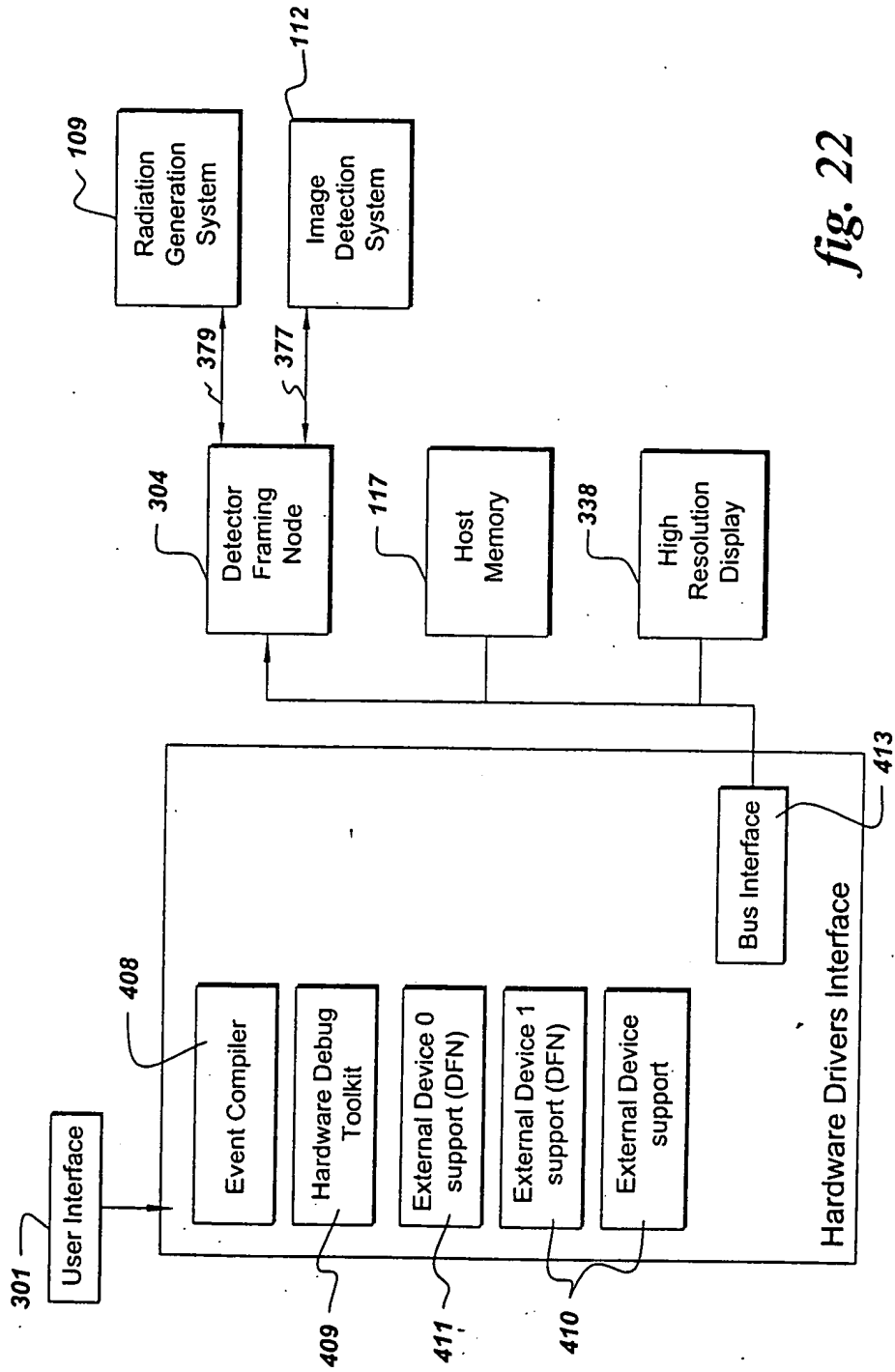


fig. 22

22/53

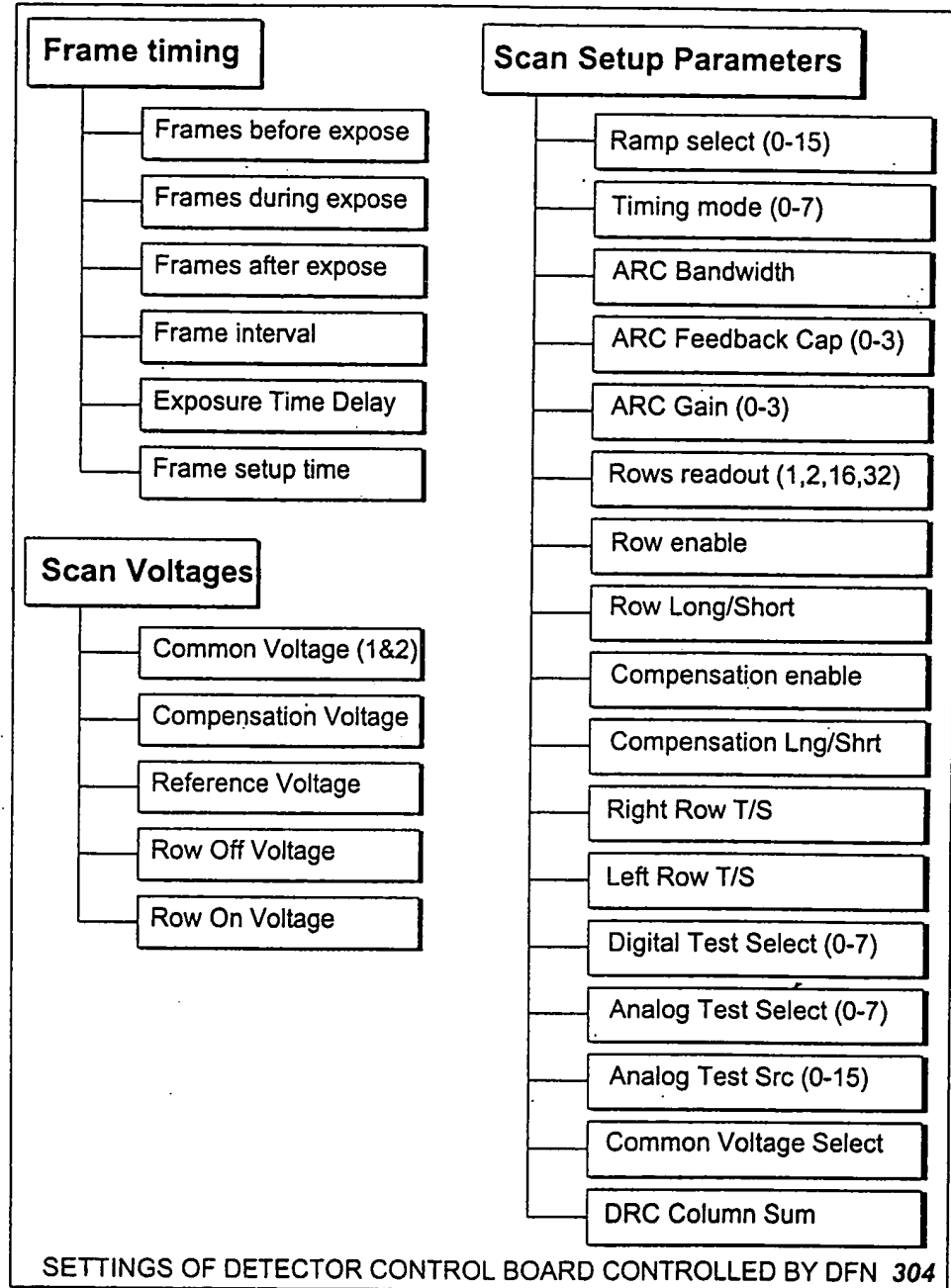


fig. 23

23/53

0074549.050001

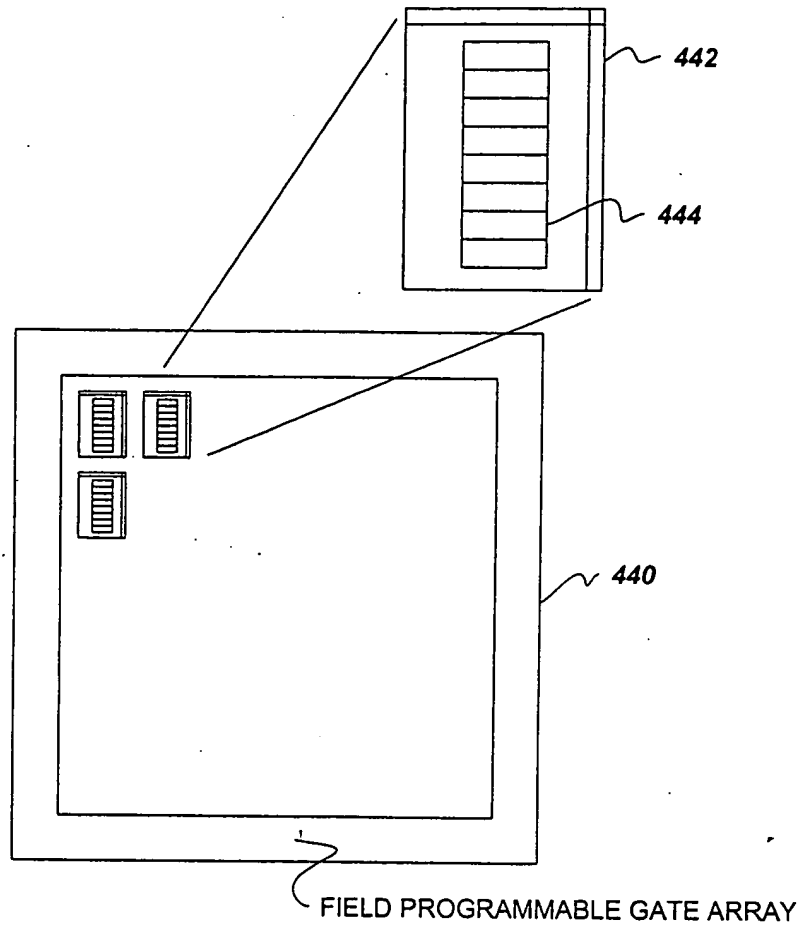


fig. 24

09741549.050701

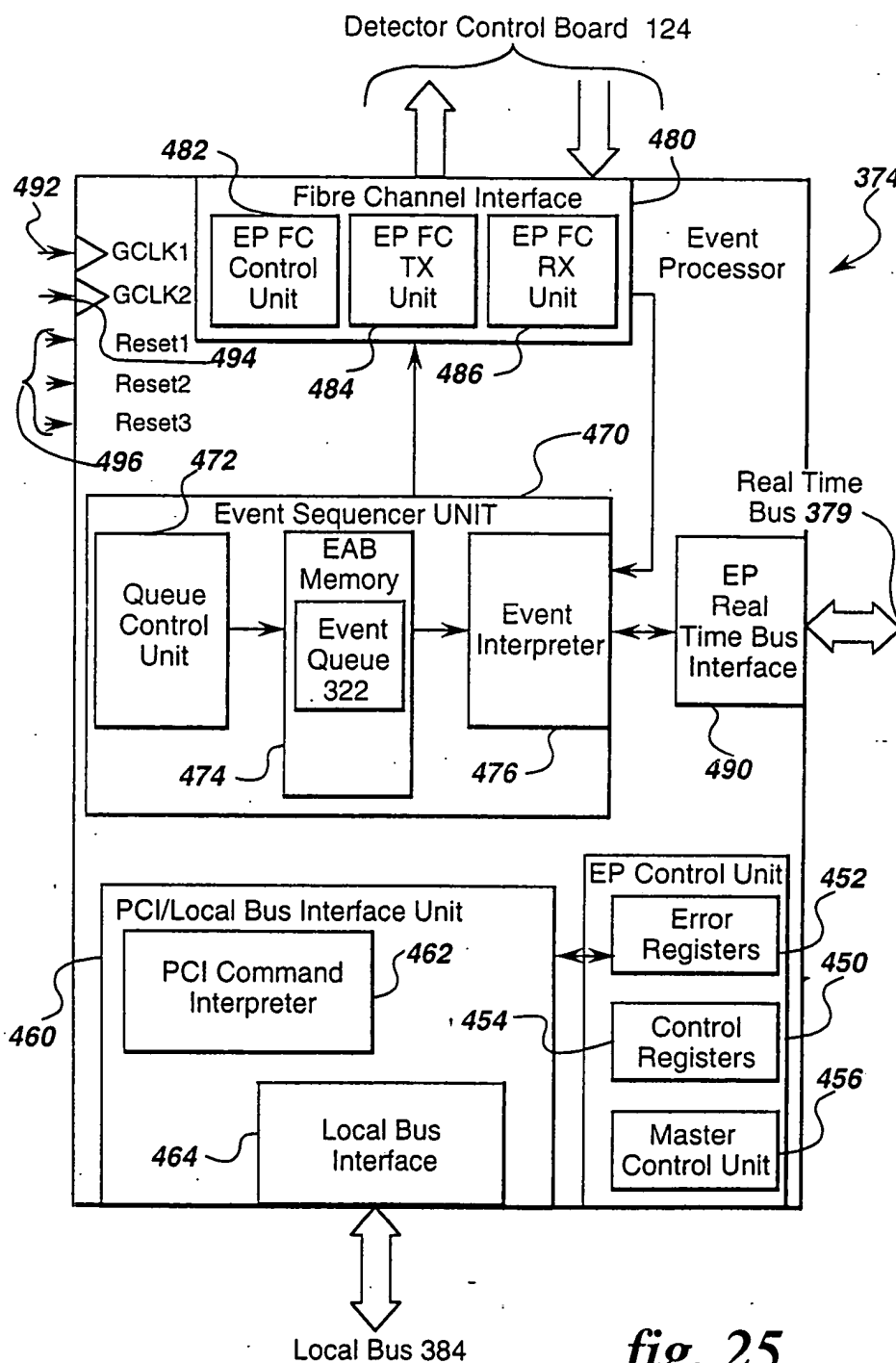


fig. 25

03741549-050701
104050-54514260

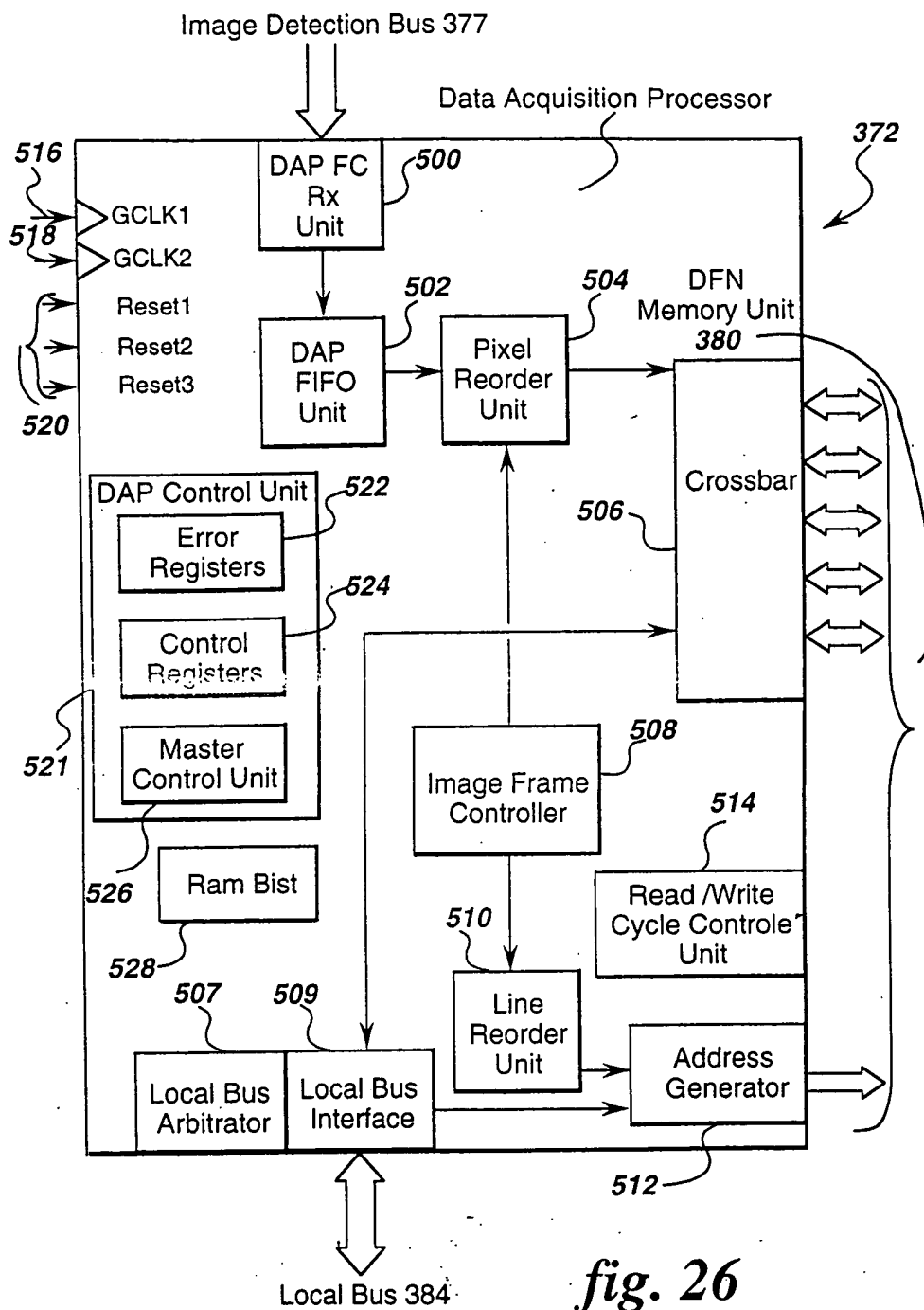
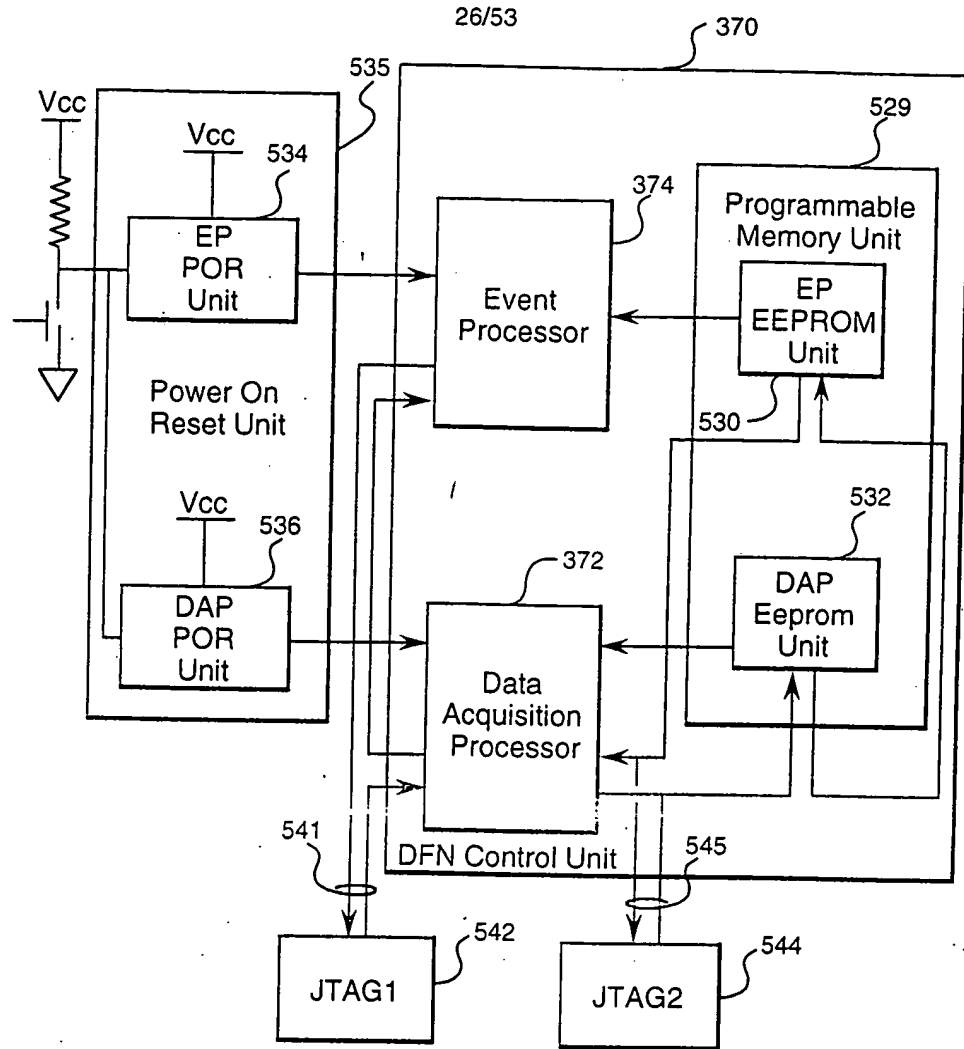


fig. 26



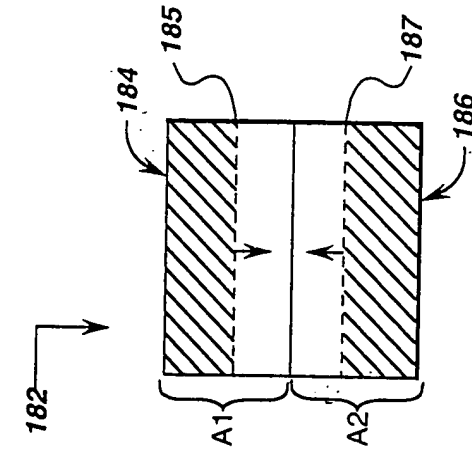


fig. 28

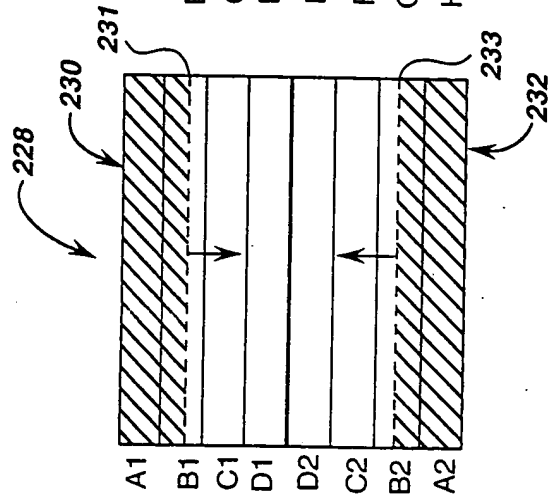


fig. 29

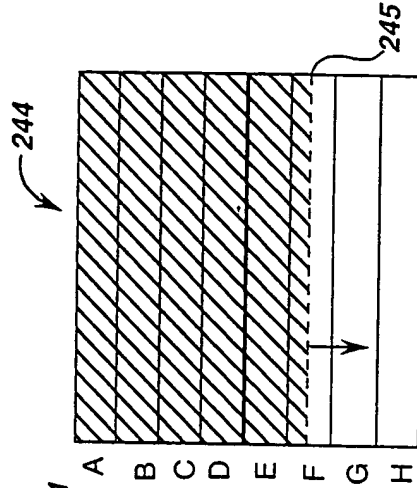


fig. 30

FO2050" 64541260

28/53

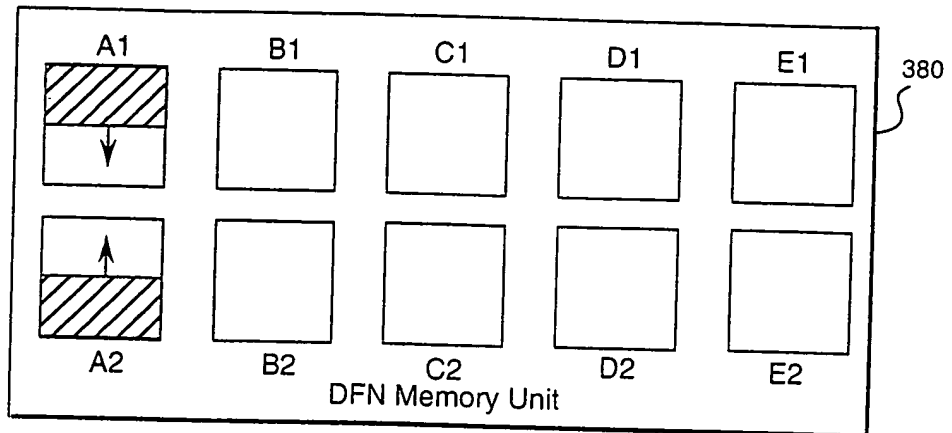


fig. 31

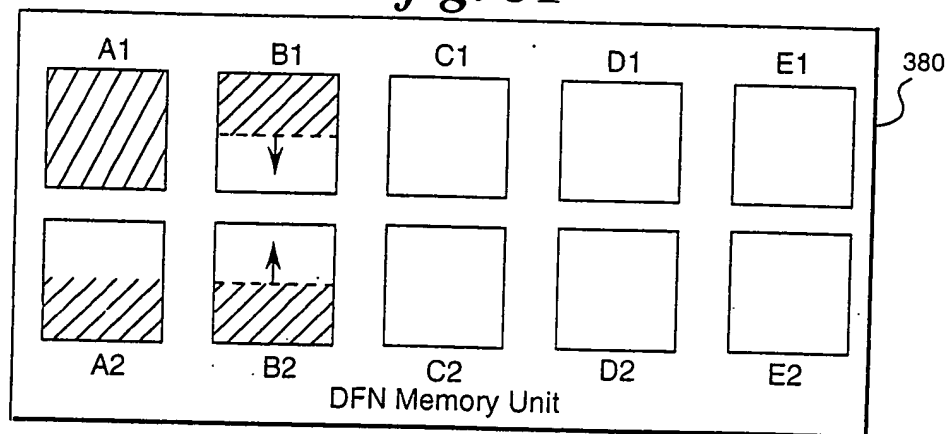


fig. 32

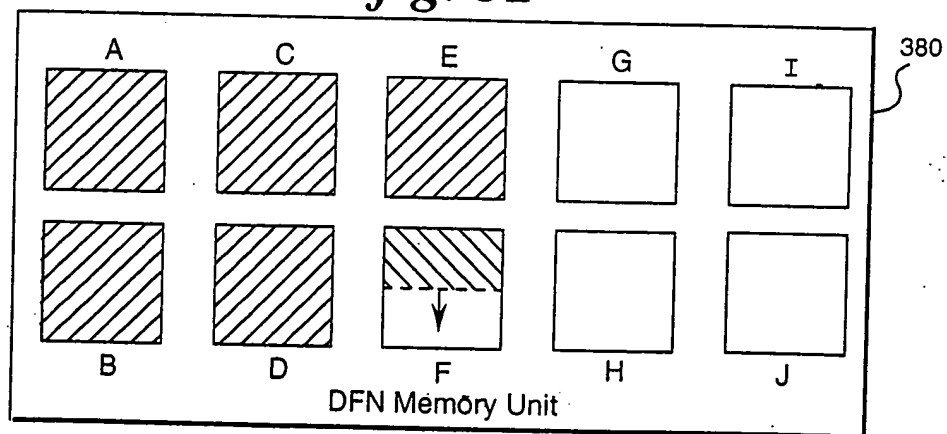


fig. 33

00774549-050701

FIG. 34

334

A1	A2
----	----

fig. 34

334

A1
B1
C1
D1
D2
C2
B2
A2

fig. 35

334

A
B
C
D
E
F
G
H

fig. 36

FIG. 37

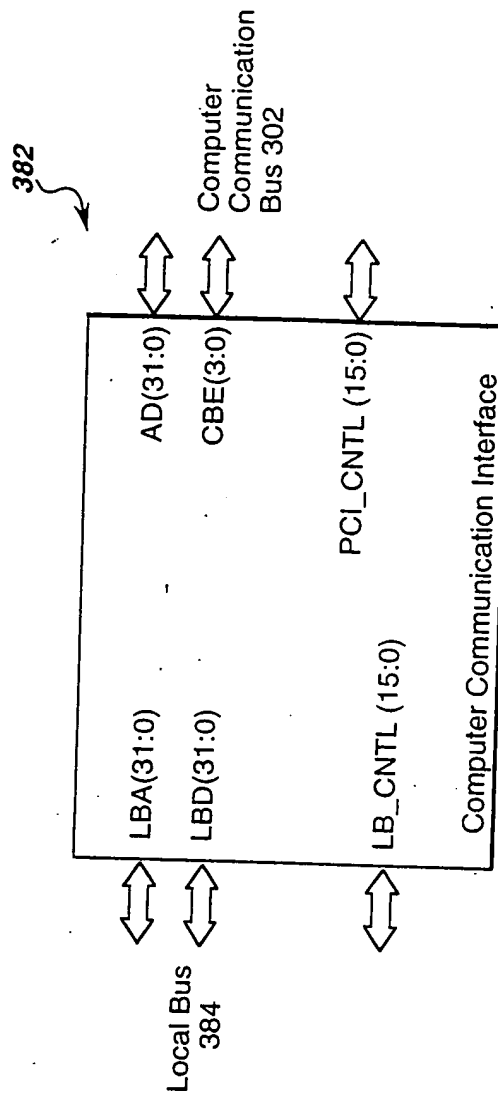


fig. 37

31/53

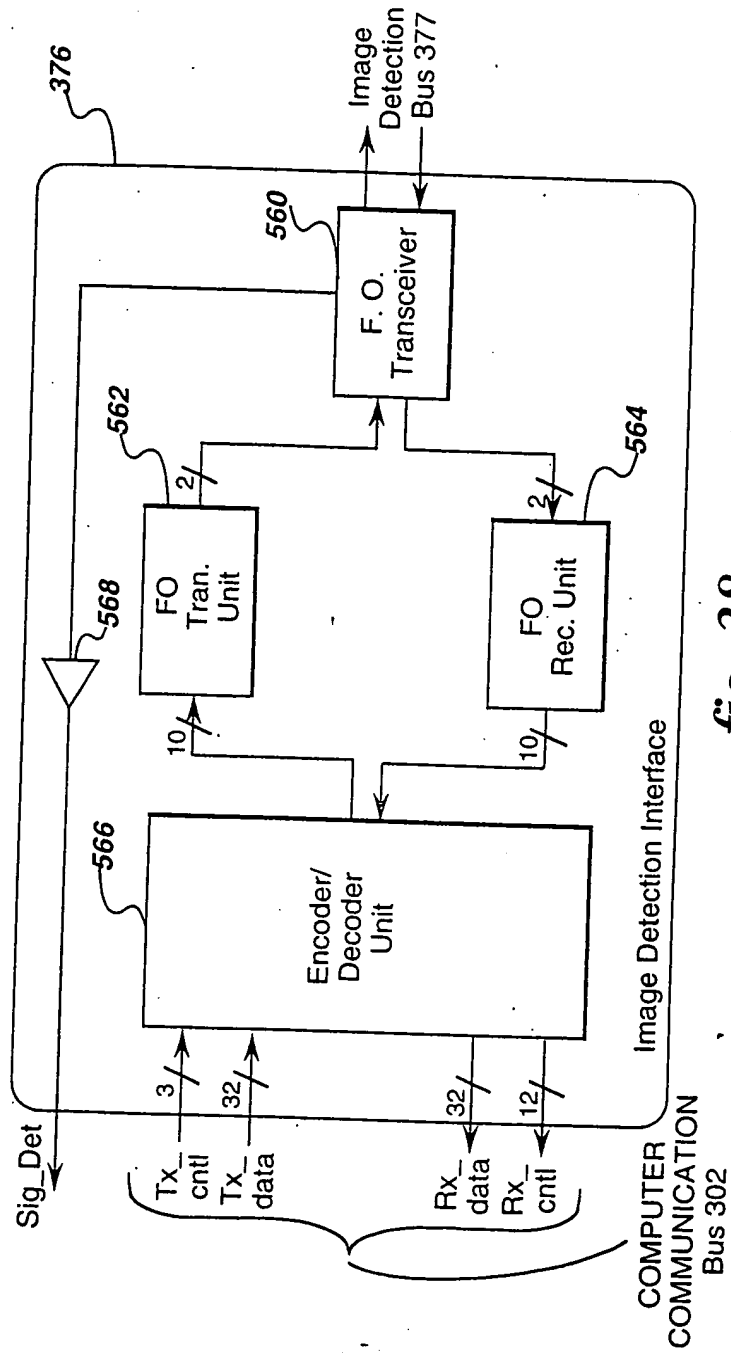


fig. 38

FO20250" 61544260

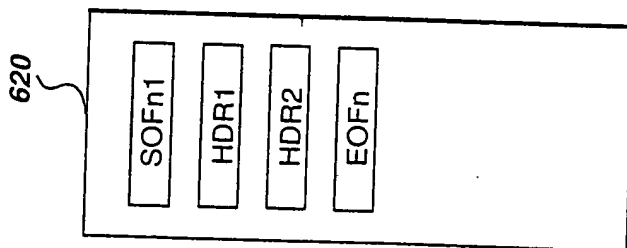


fig. 39

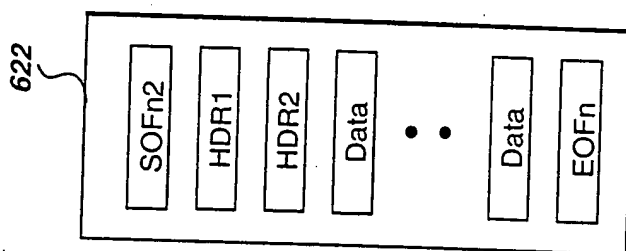


fig. 40

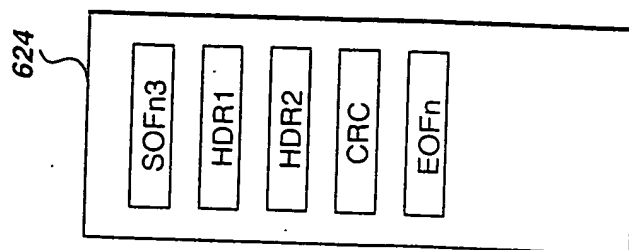


fig. 41

FIG. 39

33/53

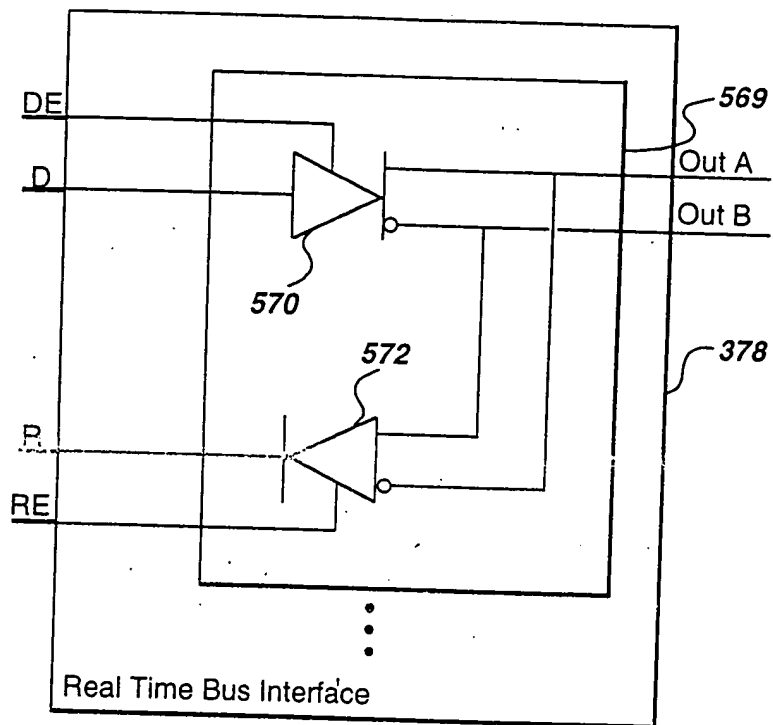
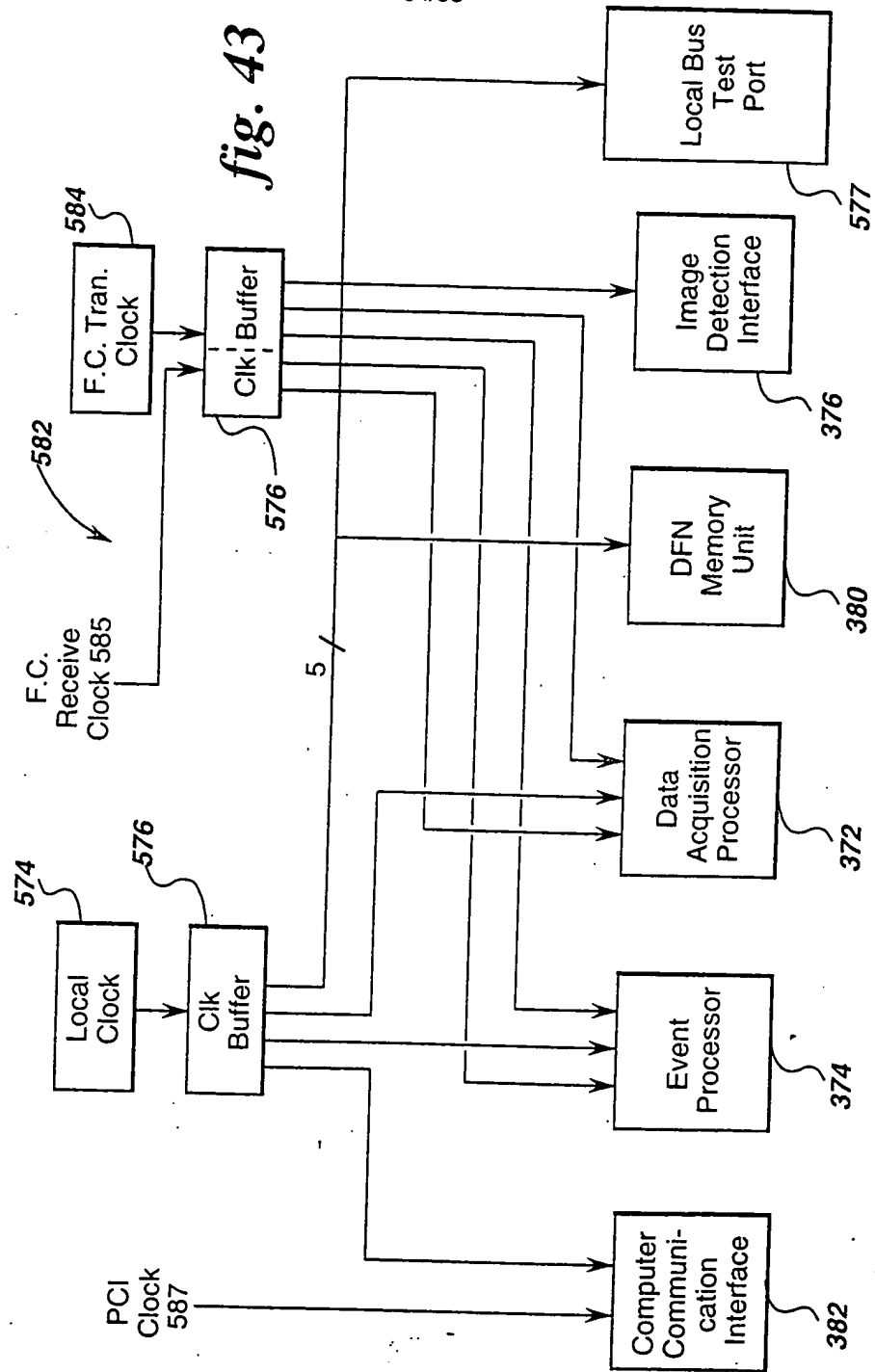


fig. 42

34/53

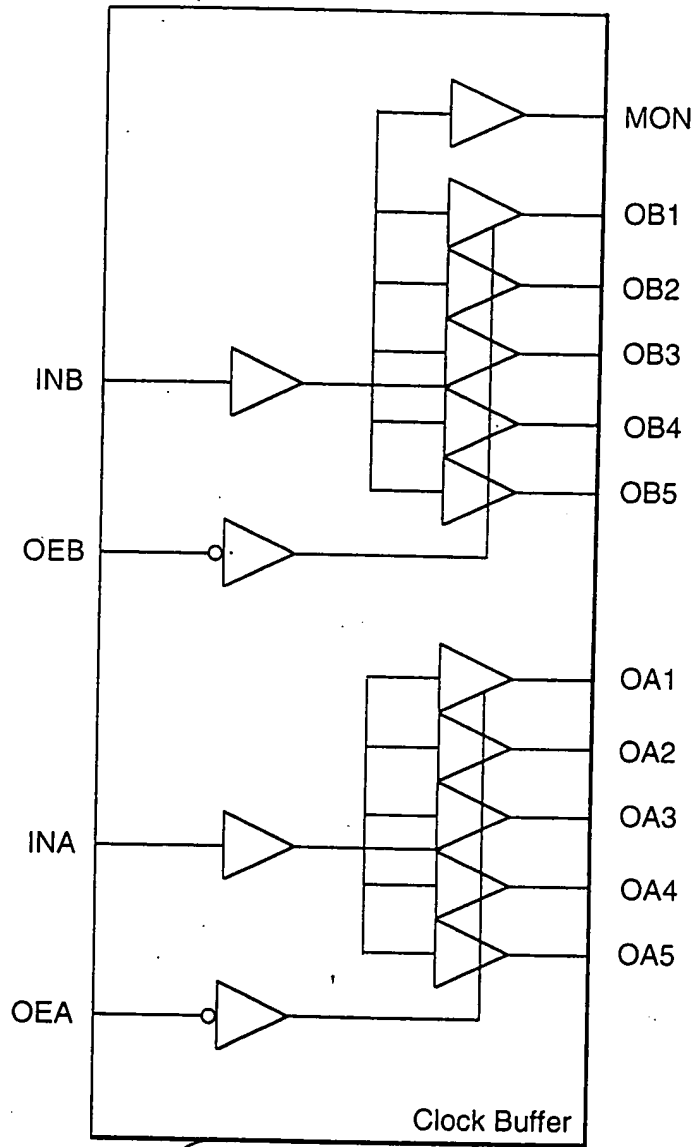
fig. 43

FIG. 43



35/53

0074519-050701



576

fig. 44

FIG. 45

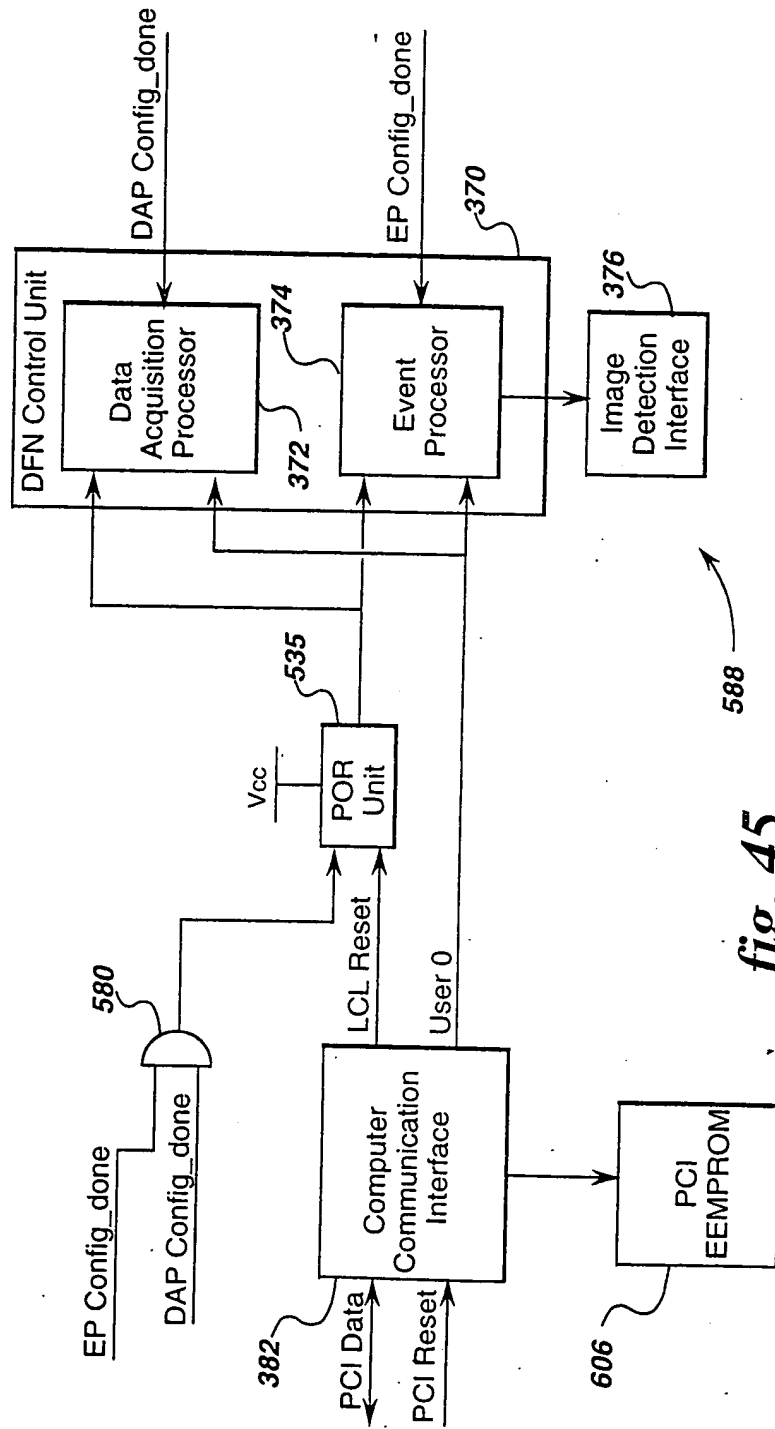
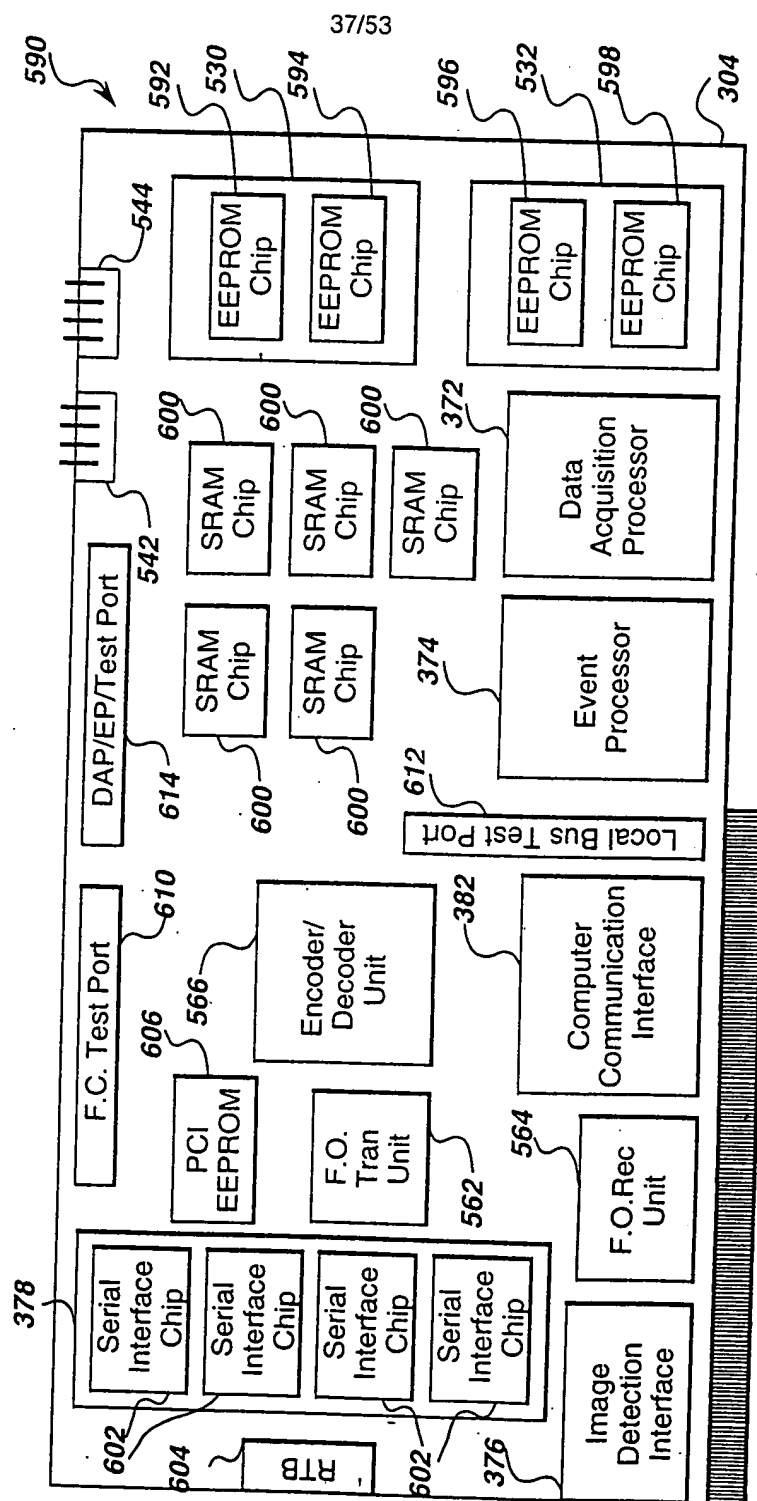
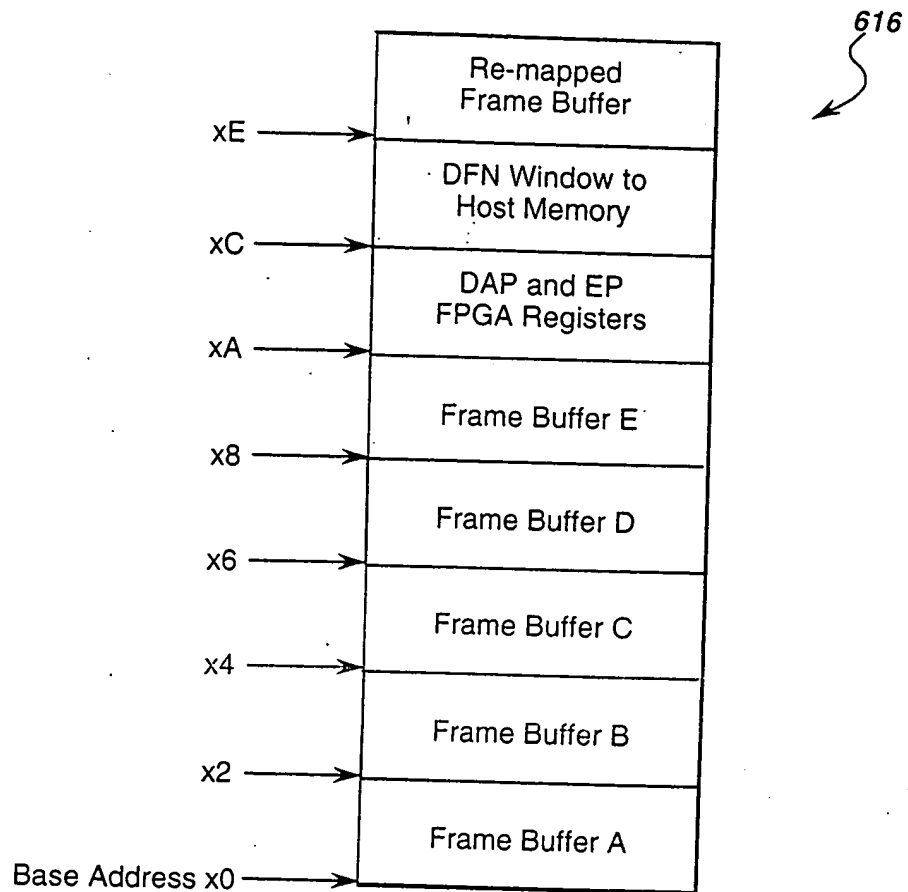


fig. 45



38/53

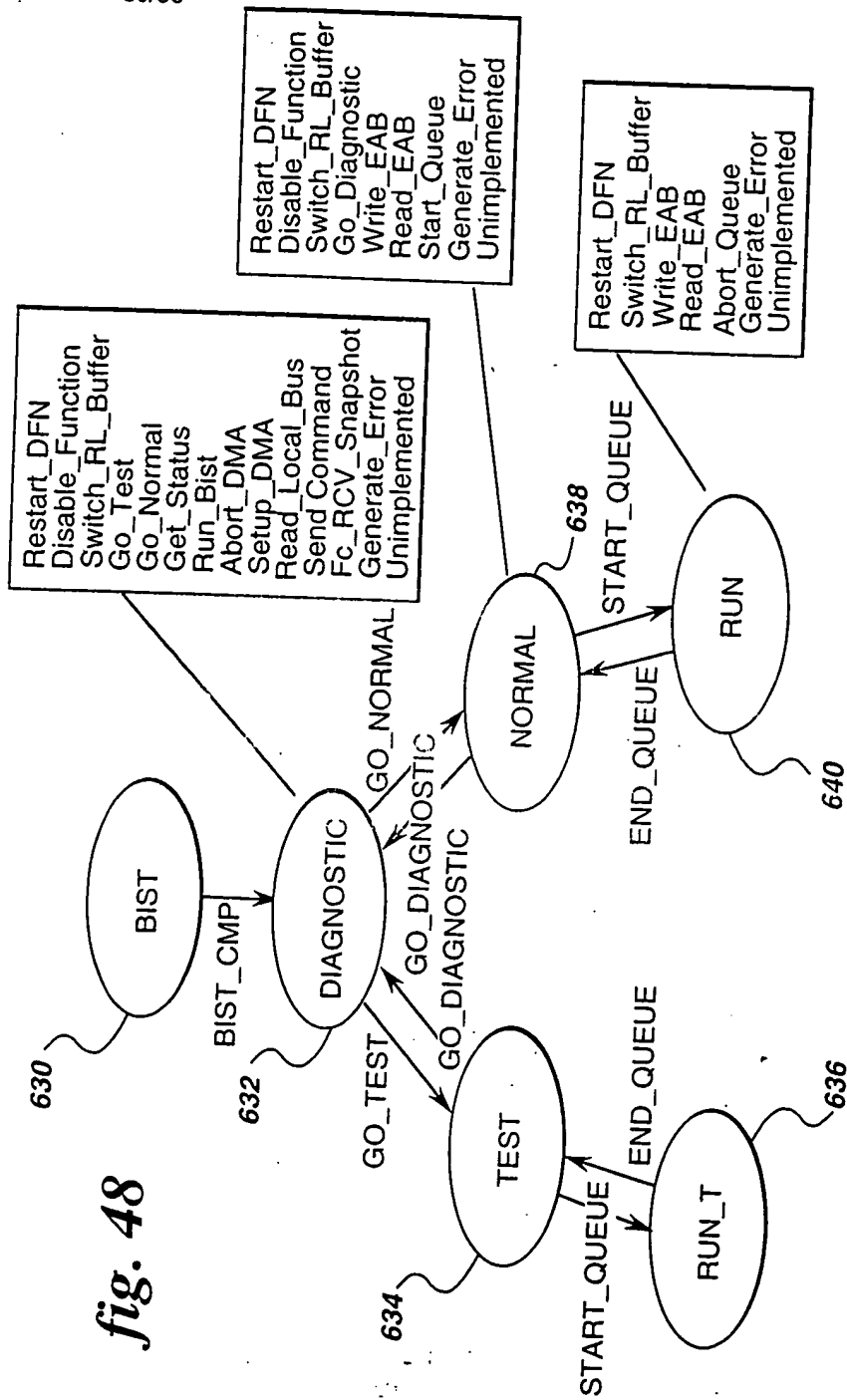


Mapping of 16 MByte PCI Address Space

fig. 47

39/53

FIG. 48



FOCUS 6454260

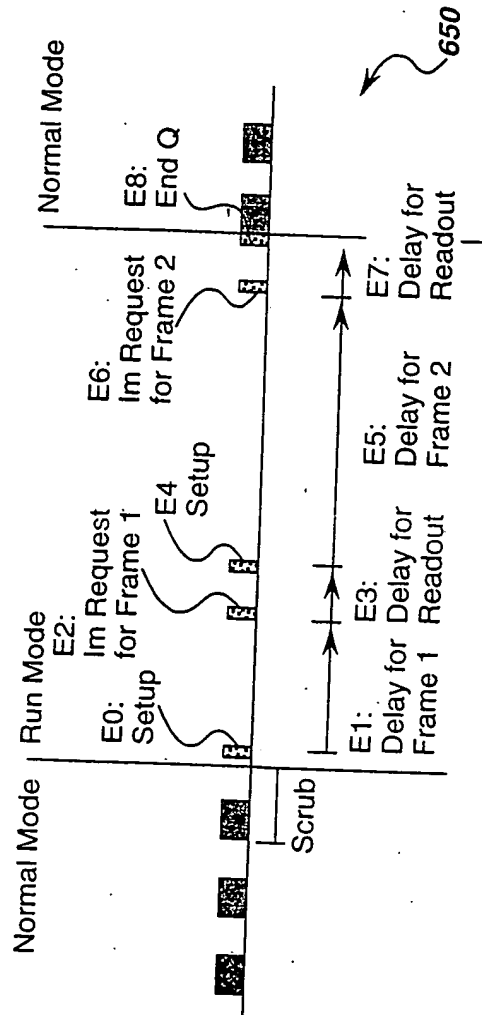


fig. 49

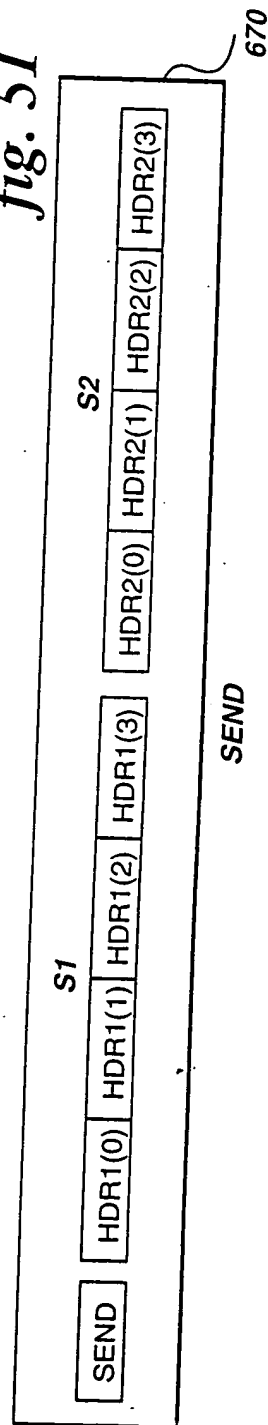
102050" 615H2260

660

Event Mnemonic	Event (showing size of arguments)	Op Code (hex)	Data (bytes)	Total (bytes)
Endq	Endq	14	0	1
Delay (T)	Delay (0xff ff ff)	10	4	5
Send (command, value)	Send (0xff ff ff ff, 0xff ff ff)	04	8	9
LoopKN (K, N)	LoopKN (0xff ff, 0xff)	0C	3	4
LoopKF (K, F)	LoopKF (0xff ff, 0xff ff ff)	0D	5	6
Wait (F)	Wait (0xff ff, ff)	09	3	4
Flag (F)	Flag (0xff ff, ff)	08	3	4

fig. 50

fig. 51



107050" CHN4260

Error Mnemonic	Description of Error
FC_TIMEOUT	Timeout Expired With No ACK Detected
FC_BAD_ACK	ACK Did Not Match Transmitted Command
FC_EXTRA_ACK	Unexpected ACK Received
FC_EXTRA_CMD	New Send Event While Waiting for ACK From Previous Send
SIG_DETN	No Input Signal Power on Fibre Channel (Cable Disconnected?)
RXERROR	Fibre Channel Receiver Detected Bad Data (Defective Chipset?)
WRDSYNCN	Fibre Channel Data Link Unsynchronized
CRXS(1)	Bad Received CRC Detected (Fiber-optic Cable Problem?)
CRXS(3) & CRXS(2)	Bad Order in Link State Machine (Defective Chipset?)

672

fig. 52

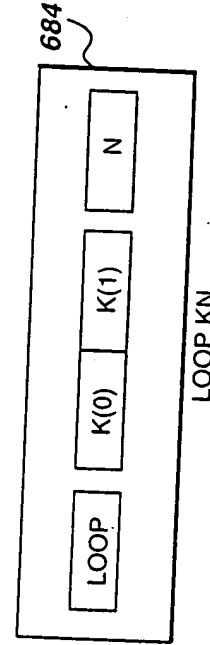
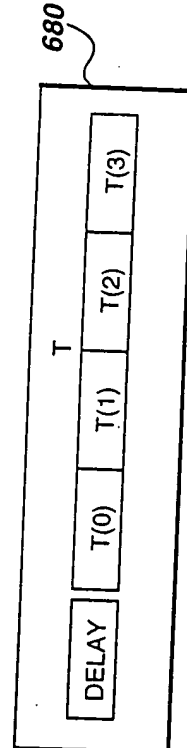


fig. 53

fig. 54

FO20950" 515H4260

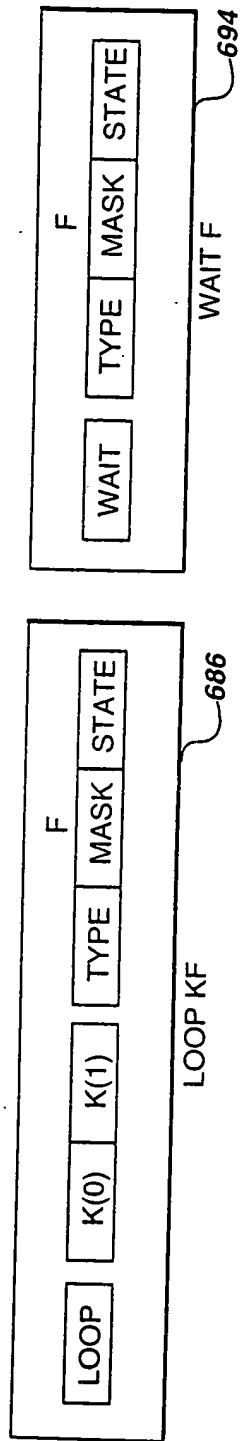


fig. 55

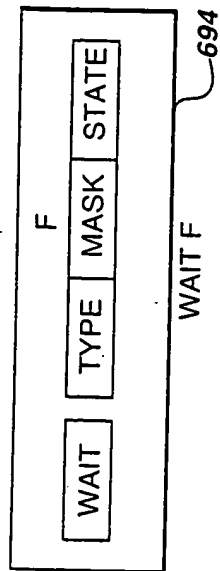


fig. 56

43/53

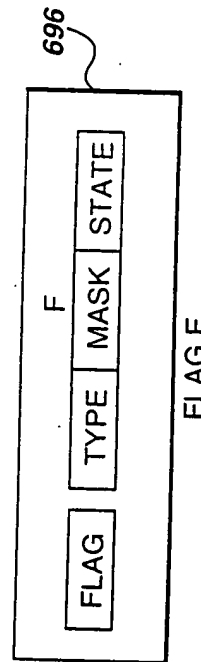


fig. 57

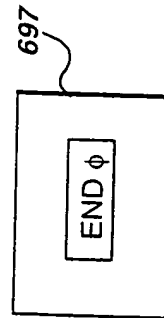


fig. 58

FIG. 59

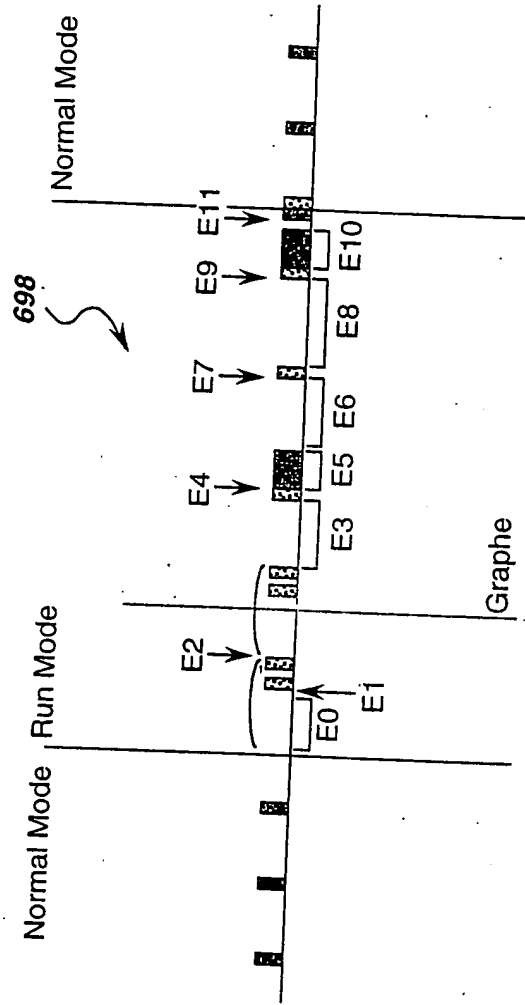


fig. 59

45/53

E11	EndQ
E10	Delay 125 ms
E9	Send Im Request
E8	Delay 500 ms
E7	Flag RT2
E6	Delay 50 ms
E5	Delay 125 ms
E4	Send Im Request
E3	Delay 300 ms
E2	Loop 2, RT1
E1	Send Scrub
E0	Delay 300 ms

EVENT QUEUE

700
↙

fig. 60

46/53

102050" CHS:4450

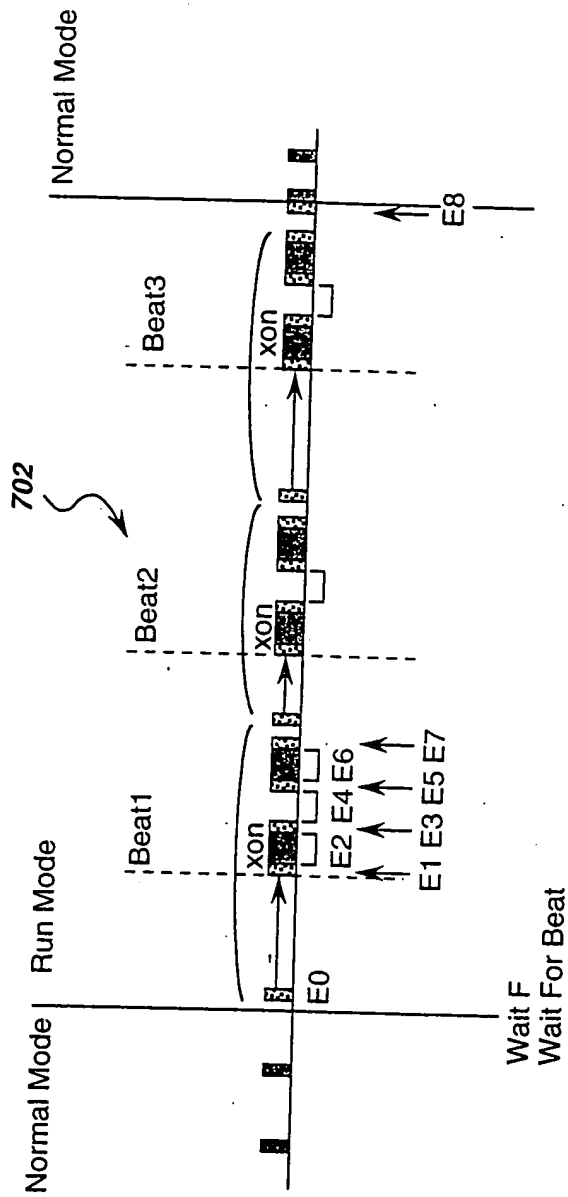
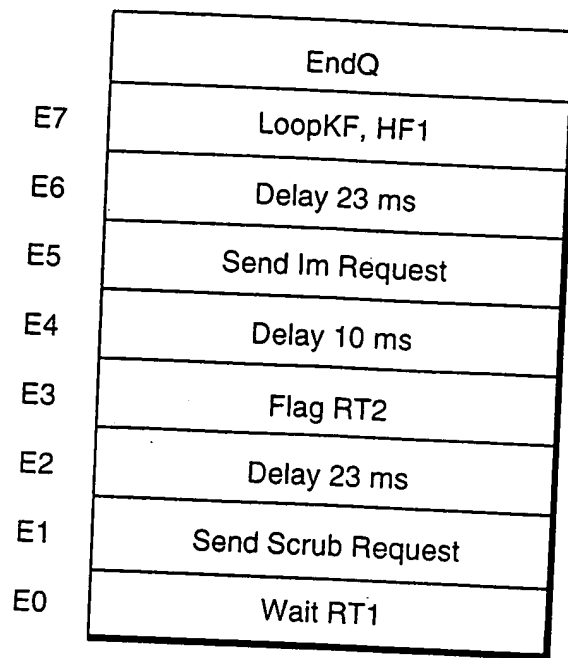


fig. 61

47/53



704

fig. 62

EVENT QUEUE

CONSTANT DATA

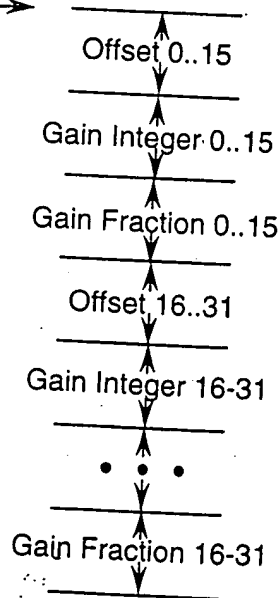


fig. 70

Constant Memory Format

0077519-050701

102050" 61544260

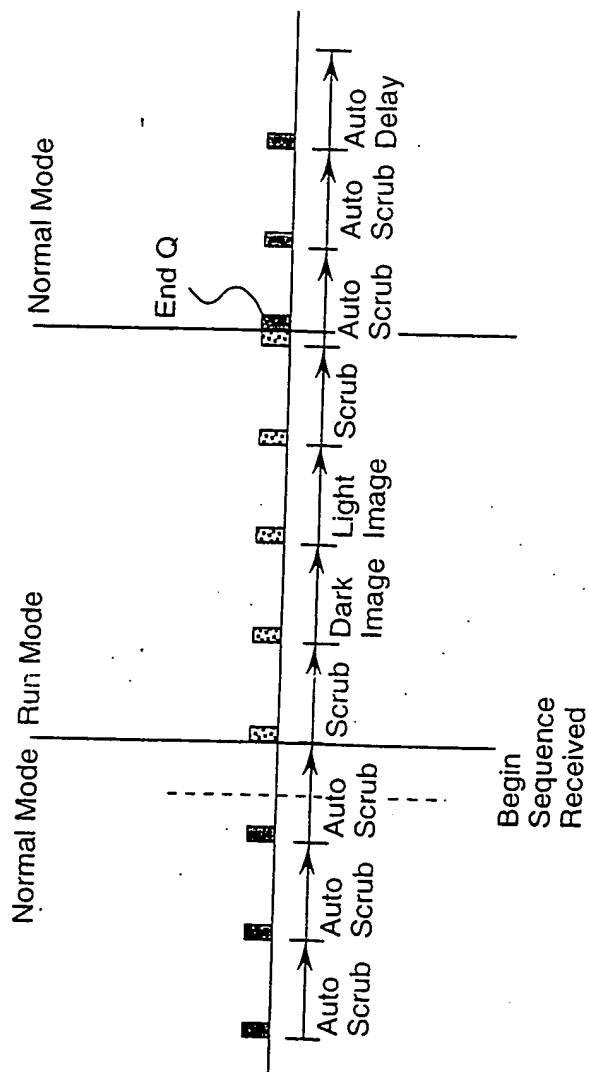


fig. 63

FOI 2009-04544260

```
sequence_begin ( );
# define qv defaults:
%qv1 =('delay_qv' => 5000);
# call frame with qv's
frame_type1(NULL, %qv1, 1);
sequence_end ( );
```

fig. 64

```
sub frame
{
$QV = 'frame';
%qv = ('delay_qv' => [10000]);
%qp = ( );
compile_init (@_, %qp, %qv, $QVf);
Delay('Delay_qv1');
compile_finit ( );
}
```

fig. 65

49/53

```
pDFN->DFNChangeQueueVariable
(
(char *) SymName,
(char *) sndBuf,
BufSize
(ULONG *) & Debug
);
```

```
// variable name
// new value
// num bytes to write
// developer info
```

fig. 66

1020950" 6:15:11:260

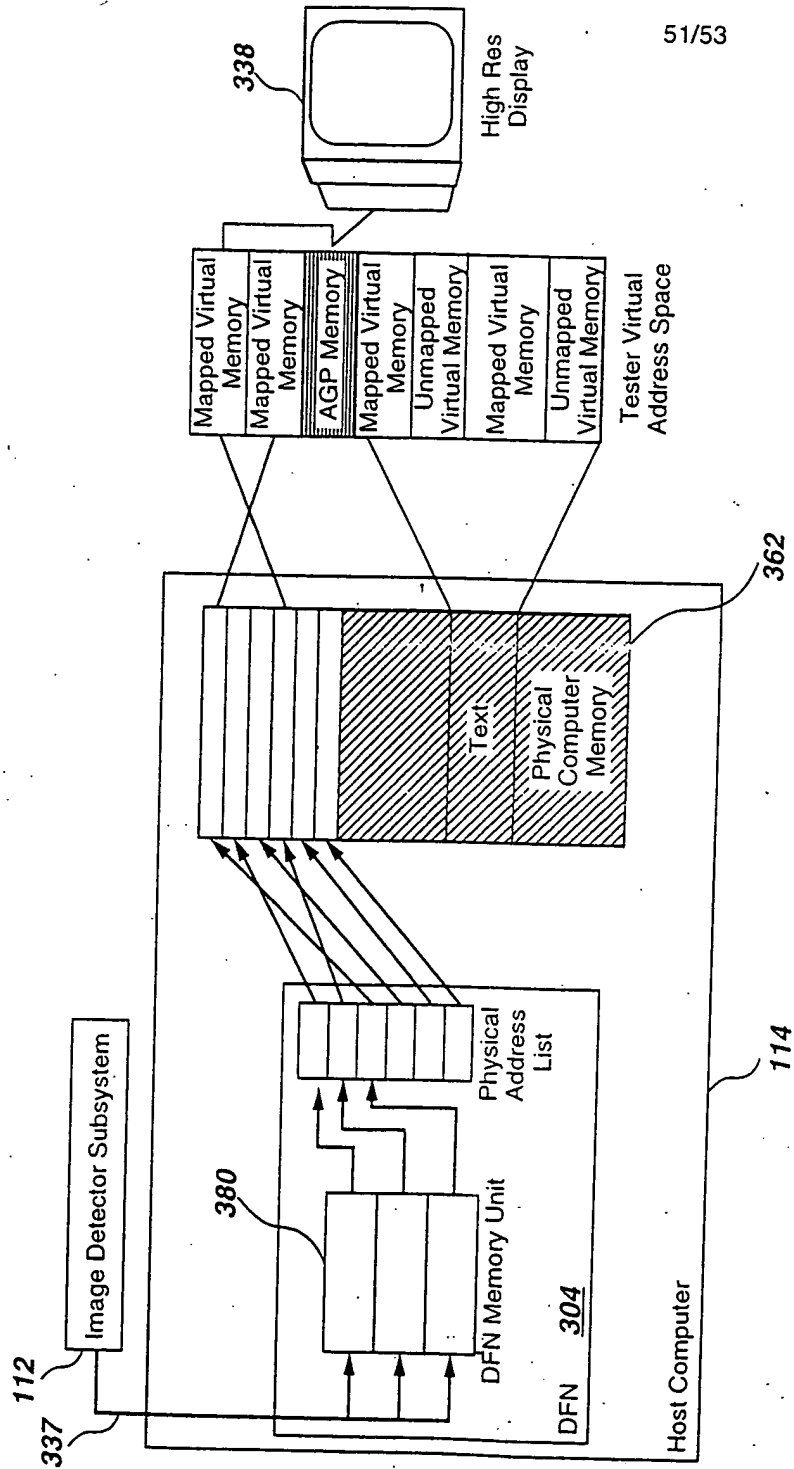
```
// load and run the event sequence
PDFNBeginSequenceNoMappingNoLog
(snum, "d:\\HF.bin");
// assign data to be passed
sndBuf = 25000;
// change the queue variable
PDFN->DFNChange QueueVariable
(
    (char *) SymName,           // variable name
    (char *) sndBuf,           // new value
    (ULONG ) sizeof sndBuf     // num bytes to write
    (ULONG *) & debug         // developer info
);
```

fig. 67

```
sub frame_type1
(
    $HFfrm = 'frame_type1';
    %qv = ('delay_qv' = > [20000]);
    %qp = ( );
    $image_cmd = [0x800000, 0x0];
    compile_init (@_, \%qp, \%qv, $HFfrm);
    Send ( $image_cmd);
    Delay('delay_qv');
    LoopKF(2, 0xA AFF01);
    compile_finit( );
)
```

fig. 68

1040501 64514260



51/53

fig. 69

52/53

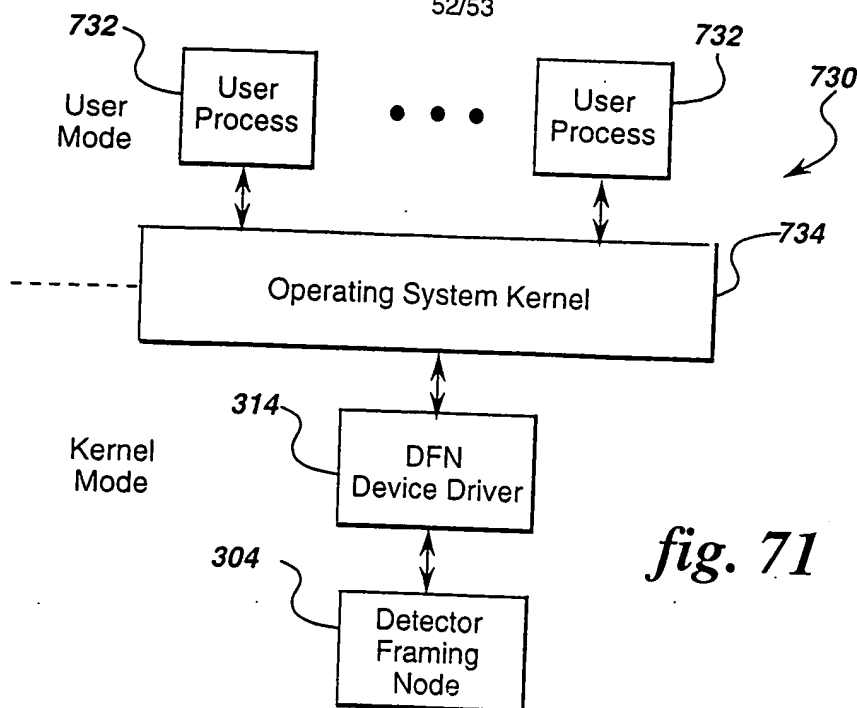


fig. 71

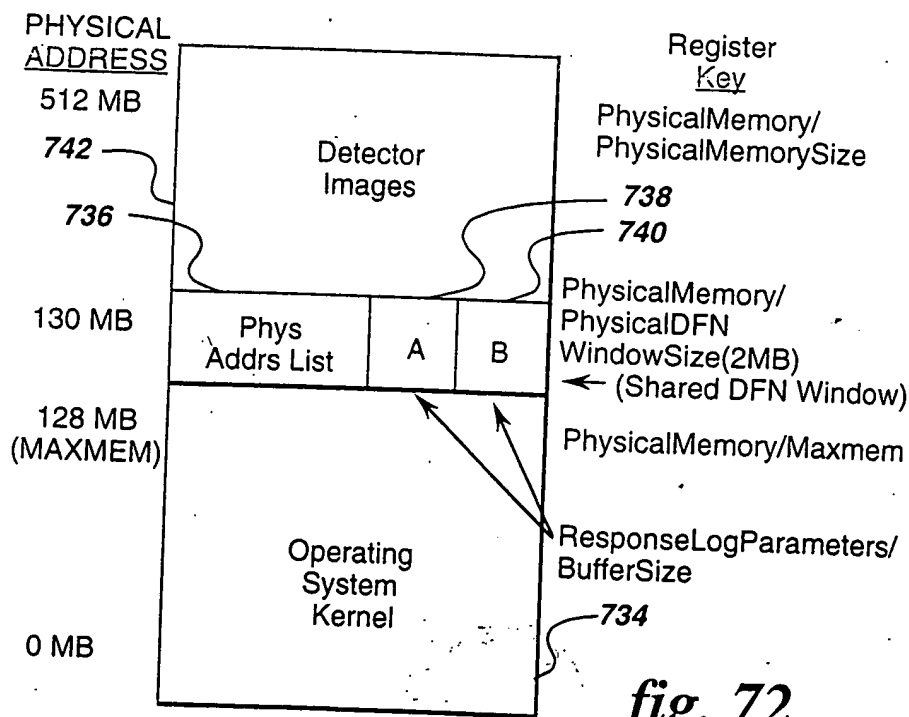


fig. 72

0374549-050701

53/53

0074549-050701

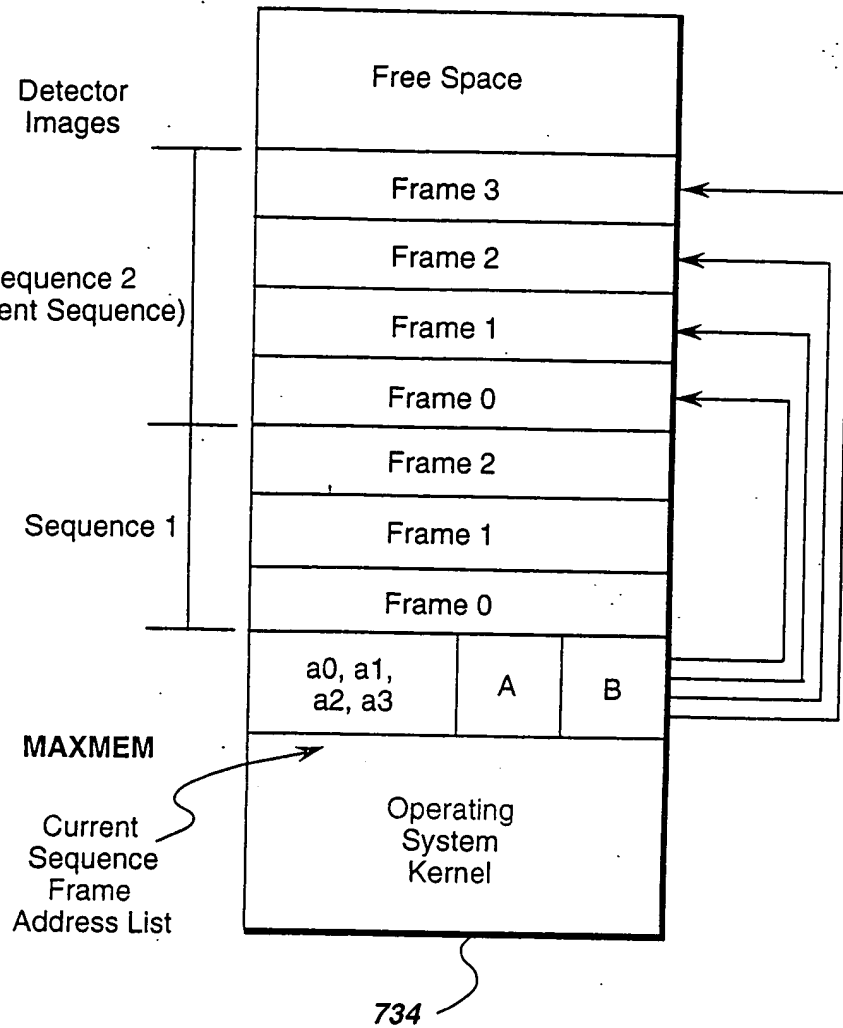


fig. 73